Embedding the Pi-Calculus into a Concurrent Functional Programming Language *

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Abstract. Correctness of program transformations and translations in concurrent programming is the focus of our research. In this case study the relation of the synchronous pi-calculus and a core language of Concurrent Haskell (CH) with asynchronous communication is investigated. We show that CH embraces the synchronous pi-calculus. The formal foundations are contextual semantics in both languages, where may- as well as should-convergence are observed. We succeed in defining and proving smart properties of a particular translation mapping the synchronous pi-calculus into CH. This implies that pi-processes are error-free if and only if their translation is an error-free CH-program. Our result shows that the chosen semantics is not only powerful, but can also be applied in concrete and technically complex situations. The developed translation uses private names. We also automatically check potentially correct translations that use global names instead of private names. As a complexity parameter we use the number of MVars introduced by the transformation, where MVars are synchronized 1-place buffers. The automated refutation of incorrect translations leads to a classification of potentially correct translations, and to the conjecture that one global MVar is insufficient.

Keywords: pi-calculus, functional programming, concurrency, adequate translations

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1 Introduction

Motivation and Goals. Our goals are the correct compilation and optimization of programs, correctness of transformations, the comparison of programming languages, and properties of translations. We already showed that contextual semantics of concurrent programming languages is powerful and that it is effectively possible to verify correctness of transformations [15, 25, 26], also under the premise not to worsen the runtime [31]. Testing may-and should-convergence in the contextual semantics turns out to be rather powerful, and has been used for concurrent functional calculi to transform and optimize programs, where for a large set of program transformations the computations are feasible.

We want to apply our techniques to the translation of programming languages, which is even more ambitious, since the contextual semantics is defined independently for the involved languages and their respective evaluations. In [30, 32] we developed notions of correctness of translations w.r.t. contextual semantics and gave several (introductory) examples. In [33] we applied these techniques in the context of concurrency, but however, there the source and the target language are quite similar (they are variations of the same concurrent call-by-value lambda calculus with shared memory). To underpin the techniques and to take a next step in our research we were looking for more interesting examples where source and target language are both concurrent but also quite different. That is why we translate a synchronous message passing model into an (asynchronous) shared memory model. Thus, as a case study we investigate the potential translations of a synchronous $\pi$-calculus into a core-language of Concurrent Haskell, the calculus $CH$.

The contextual semantics of concurrent programming languages is a generalization of the extensionality principle of functions. The test for a program $P$ is whether $C[P]$ for all program-contexts $C$ successfully terminates (converges) or not, which usually means that the standard reduction sequence ends with a value. For a concurrent program $P$, we use two observations: may-convergence ($P \downarrow$) – at least one execution path terminates successfully, and should-convergence ($P \downarrow\downarrow$) – every intermediate state of a reduction sequence may-converges. For two processes $P_1, P_2$, $P_1 \leq_c P_2$ holds iff for all contexts $C$: $(C[P_1] \downarrow \implies C[P_2] \downarrow)$ and $(C[P_1] \downarrow\downarrow \implies C[P_2] \downarrow\downarrow)$, and $P_1 \sim_c P_2$ (are equivalent) iff $P_1 \leq_c P_2$ and $P_2 \leq_c P_1$. Processes $P_1$ and $P_2$ are contextually equivalent, $P_1 \sim_c P_2$, iff $P_1 \leq_c P_2$ and $P_2 \leq_c P_1$. Showing equal expressivity of two (concurrent) calculi by a translation $\tau$ is founded on properties of $\tau$: this requires that may- and should-convergence make sense in each calculus. An important property is adequacy (see Definition 4.4), which holds if $\tau(P_1) \leq_c \tau(P_2)$ implies $P_1 \leq_c P_2$, for the contextual preorders and for all processes $P_1, P_2$. Full-abstractness, i.e. $\tau(P_1) \leq_c \tau(P_2)$ iff $P_1 \leq_c P_2$, only holds in the rare case that the two calculi are more or less the

\footnote{An alternative observation is must-convergence (all execution paths terminate). The advantages of equivalence notions based on may- and should-convergence are invariance under fairness restrictions, preservation of deadlock-freedom, and equivalence of busy-wait and wait-until behavior (see e.g. [33]).}
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same. Translating the $\pi$-calculus into CH requires to find at least one very good translation. Our translation $\tau_0$ (defined and analyzed in Sect. 4) is one of these.

Source and Target Calculi. The well-known $\pi$-calculus [14,13,29] is a minimal model for mobile and concurrent processes. Data-flow is possible by communication between processes, i.e. by passing messages between them. Channel names are sent as messages, and processes and links between processes can be dynamically created and removed which together makes processes mobile. The interest in the $\pi$-calculus is not only due to the fact that it is used and extended for various applications, like reasoning about cryptographic protocols [1], applications in molecular biology [20], and distributed computing [11,7]. The $\pi$-calculus also permits the study of intrinsic principles and semantics of concurrency, of concurrent programming and the inherent nondeterministic behavior of mobile and communicating processes. The investigated variant of the $\pi$-calculus in this paper is the synchronous $\pi$-calculus with replication, but without sums, matching operators, or recursion. To observe termination of a process, the investigated $\pi$-calculus has a constant $\text{Stop}$ which signals successful termination.

One important reason, why use Concurrent Haskell as a target language, and not sequential Haskell, is that for a correct translation the target language must be able to perform a parallel convergence test [21,19], since the $\pi$-calculus has one (i.e. by the context $[\cdot] | [\cdot]$). Core-Haskell with erratic choice (i.e., a construct that permits to choose between two expressions) appears to be more expressive, however, this and similar calculi do not have a parallel-convergence testing context $P[\cdot, \cdot]$. Informally, the reason is that if $P[v, \bot]$ as well as $P[\bot, v]$ converge for values $v$, then in such programming languages $P[e_1, e_2]$ has to converge independently of expressions $e_1, e_2$ (in particular, $P[\bot, \bot]$ must also converge). Note that there are stronger nondeterministic primitives, like McCarthy’s $\text{amb}$-operator [12], which can express parallel-convergence testing contexts (see e.g. [24]). Concurrent Haskell [18,9] is very powerful and has a parallel-convergence testing context, which also holds for CH. The calculus CH, is a process calculus where threads evaluate expressions from a lambda calculus extended by data constructors, case-expressions, recursive let-expressions, and Haskell’s seq-operator. Also monadic operations (sequencing and creating threads) are available. The shared memory is modeled by MVars (mutable variables) which are one-place buffers that can be either filled or empty and are blocking if a thread tries to fill a full buffer or to empty an empty buffer. The calculus CH is a variant (or a subcalculus) of the calculus CHF [25,26] which extends Concurrent Haskell by futures. A major advantage of this approach is that we can reuse studies and results on the contextual semantics of CHF also for CH.

Details and Variations of the Translation One main issue for a correct translation from $\pi$-processes to CH-programs is to correctly encode the synchronous communication of the $\pi$-calculus. The problem is that the so-called MVars in CH (see also [18]) have an asynchronous behavior. To implement synchronous communication the weaker synchronization property of MVars has to be exploited, where we must be aware of the potential interferences of the executions of other translated communications on the same channel. The task of
finding such translations is reminiscent of the channel-encoding used in [18], but, however, there an asynchronous channel is implemented while we look for synchronous communication.

We provide a translation $\tau_0$ which uses a private MVar per channel to ensure that no other sender or receiver can interfere with the interaction. A similar idea was used in [10,3] in a different scenario. We succeed in mathematically confirming that the translation $\tau_0$ has strong and nice semantic properties.

Since we are also interested in simpler translations (e.g. using private names is complex, since it generates a new MVar for each communication), we looked for correct translations with a fixed and static number of MVars per channel in the $\pi$-calculus. Since this task is too complex and error-prone for hand-crafting, we automated it by implementing a procedure to rule out incorrect translations.\footnote{The tool and some output generated by the tool are available via \url{https://gitlab.com/davidsabel/refute-pi}.}

Thereby we assumed per communication channel a single MVar for exchanging the channel-name and perhaps several additional MVars of unit type to perform checks whether the message was send / or received (we call them check-MVars).

The outcomes of our automated search are a correct translation that uses two check-MVars, where one is used as a mutex between all senders or receivers on one channel, and correct translations using three additional MVars where the filling and emptying operations for each MVar must not come from the same sender or receiver. The experiments lead to the conjecture that there is no translation using only one check-MVar.

**Results.** Our novel results are a translation $\tau_0$ from the $\pi$-calculus into $CH$, adequacy of the open translation $\tau$ (Theorem 4.10), and full abstraction of $\tau$ on closed $\pi$-processes (Theorem 4.12). From a technical point of view, a novelty is the comparison of the $\pi$-calculus with a concurrent programming language using contextual semantics for may-convergence and should-convergence in both calculi, which is technically involved since the syntactic details of the standard reductions in both calculi have to be analyzed. The adaptation of the adequacy and full abstraction notions (Definition 4.4) for open processes is also novel extending our work in [30,32]. We further define a quite general formalism for the representation of translations with global names and analyze different classes of such translations by an automated tool. In particular, we show correctness of two particular of these translations in Theorems 5.9 and 5.11.

The discovered correct translations look quite simple and their correctness seems to be quite intuitive. However, our experience is that searching for correct translations is quite hard. We “found” several translations which also were simple and seemed to be intuitively correct. However, they were wrong. Our automated tool helped us to rule out wrong translations.

**Further Related Work.** Encodings of synchronous communication by asynchronous communication using a private name mechanism are given in [10,3] for (variants of the) $\pi$-calculus. Our idea of the translation $\tau_0$ similarly uses a private MVar to encode the channel based communication, but clearly our setting is different, since our target language is Concurrent Haskell. Encodings between
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π-calculi with synchronous and with asynchronous communication were, for instance, already considered in \[10,13,17\], where encodability results are obtained for the π-calculus without sums \[10,13\], while in \[17\] the expressiveness of synchronous and asynchronous communication in the π-calculus with mixed sums was compared and non-encodability is a main result. Work on translations of the π-calculus into programming calculi and logical systems is \[2\], where a translation into a graph-rewriting calculus is given and soundness and completeness w.r.t. the operational behavior is proved. The article \[34\] shows a translation and a proof that the π-calculus is exactly operationally represented. There are several works on session types which are related to the π-calculus, e.g., \[16\] studies encodings from a session calculus into PCF extended by concurrency and effects and also an embedding in the other direction, mapping PCF extended by effects into a session calculus. The result is a (strong) operational correspondence between both calculi. Encodings of CML-events in Concurrent Haskell using MVars are published in \[22,5\]. This approach is more high-level than ours (since we are working directly with the π-calculus). In \[5\] correctness of a distributed protocol for selective-communication w.r.t. an excerpt of CML is shown, and correctness of implementing the protocol in the π-calculus is proved. The protocol is implemented in Concurrent-Haskell, but correctness of this part is not proven, since the paper focuses to show that CML-events are implementable in languages with first-order message-passing which is different from our focus (translating the π-calculus into CH).

**Outline.** We introduce both calculi in Sects. 2 and 3 and the translation using private names in Sect. 4. In Sect. 5 we treat translations with global names and present our automated tool. We conclude and discuss future work in Sect. 6.

2 The π-Calculus with Stop

We explain the synchronous π-calculus \[14,13,29\] without sums and with replication in a variant extended with a constant \texttt{Stop} \[27\], that signals successful termination of the whole π-calculus program. The π-calculus without \texttt{Stop} and with so-called barbed convergences \[28\] are equivalent w.r.t. contextual semantics (see Theorem \[D.4\] in the appendix). Thus, adding the constant \texttt{Stop} is not essential, however, the treatment and the translation are easier to explain for the π-calculus with \texttt{Stop}.

**Definition 2.1.** Let \(N\) be a countable set of (channel) names. The syntax of processes is shown in Fig. 7. Free names \(FN(P)\), bound names \(BN(P)\), and α-equivalence \(\alpha\)-equivalence \(=_{\alpha}\) in \(\Pi_{\text{Stop}}\) are as usual in the π-calculus. A process \(P\) is closed if \(FN(P) = \emptyset\). Let \(\Pi_{\text{Stop}}\) denote the closed processes in \(\Pi_{\text{Stop}}\).

We briefly explain the language constructs. Name restriction \(\nu x.P\) restricts the scope of name \(x\) to process \(P\), \(P1Q\) is the parallel composition of \(P\) and \(Q\), the process \(\pi y.P\) waits on channel \(x\) to output \(y\) over channel \(x\) and becoming \(P\) thereafter, the process \(x(y).P\) waits on channel \(x\) to receive input, and after
Fig. 1. Syntax of processes $\Pi_{\text{Stop}}$, process contexts $\Pi_{\text{Stop},C}$ and reduction contexts $PCxts$ where $x, y$ are names.

**Interaction rule:**

\[
\text{(in) } x(y).P | x(y).Q \xrightarrow{\text{ia}} P | x(y).Q,
\]

**Closure:** If $P \equiv D[P']$, $P' \xrightarrow{\text{ia}} Q'$, $D[Q'] \equiv Q$, and $D \in PCxts$ then $P \xrightarrow{\text{sr}} Q$

Fig. 2. Reduction rule and standard reduction in $\Pi_{\text{Stop}}$

- $P \equiv Q$, if $P \equiv_{\text{a}} Q$
- $P | (Q | R) \equiv (P | Q) | R$
- $\nu x.(P | Q) \equiv P | \nu x.Q$, if $x \notin FN(P)$
- $P | 0 \equiv P$
- $\nu x.0 \equiv 0$
- $\nu x.\text{Stop} \equiv \text{Stop}$
- $\nu x.y.P \equiv ty,x.P$
- $P | Q \equiv Q | P$
- $!P \equiv P | !P$

Fig. 3. Structural congruence

receiving the input $z$, the process turns into $P[z/y]$ (where $P[z/y]$ is the substitution of all free occurrences of name $y$ by name $z$ in process $P$), the process $!P$ denotes the replication of process $P$, i.e. it behaves like an infinite parallel combination of process $P$ with itself, the process $0$ is the silent process that does nothing, and Stop is a process constant that signals successful termination. We sometimes write $x(y)$ instead of $x(y).0$ as well as $\overline{y}x$ instead of $\overline{y}x.0$.

**Definition 2.2.** The only reduction rule of the $\Pi_{\text{Stop}}$-calculus is the so-called interaction $\xrightarrow{\text{ia}}$ which is defined in Fig. 2.

**Definition 2.3.** Let $P, Q, R$ be processes and $x, y$ channel names. Structural congruence $\equiv$ is the least congruence satisfying the laws shown in Fig. 3.

**Remark 2.4.** We did not include “new” laws for structural congruences on the constant Stop, like Stop | Stop equals Stop, or even Stop | P equals Stop. We did not want to include them, since this would require to develop a lot of theory known from the $\pi$-calculus without Stop again for the calculus with Stop. Our view is that Stop is a mechanism to have an easy notion for successful termination, that can be easily replaced by other similar notions (e.g. observing an open input or output as in barbed testing). However, it is easy to prove that those mentioned equations hold on the semantic level, e.g. the contextual equivalence $\text{Stop} | P \simeq_{c} \text{Stop}$ holds (where $\simeq_{c}$ is defined below in Definition 2.10).
As example, consider the communication that sends name \( y \) over channel \( x \) and then sends \( u \) over channel \( y \):

\[(x(z), \tau u.0 | \tau y.y(x).0) \xrightarrow{\text{sr}} (\tau u.0[y/z] | y(x).0) \equiv (\tau u.0 | y(x).0) \xrightarrow{\text{sr}} (0 | 0) \equiv 0\]

**Definition 2.5.** A process context \( C \in \Pi_{\text{Stop,C}} \) is a process that has a hole \([\cdot]\) at one process position. They are defined by the grammar shown in Fig. 2. With \( C[P] \) we denote the substitution of the hole in \( C \) by \( P \). The special class of reduction contexts is also defined in Fig. 2.

**Definition 2.6.** A standard reduction \( \xrightarrow{\text{sr}} \) is the application of \( \xrightarrow{\text{ia}} \) within a reduction context modulo structural congruence (see Fig. 3). Let \( \xrightarrow{\text{sr,n}} \) denote n standard reductions and \( \xrightarrow{\text{sr}} \) denotes the reflexive-transitive closure of \( \xrightarrow{\text{sr}} \).

**Definition 2.7.** A process \( P \in \Pi_{\text{Stop}} \) is successful, if \( P \equiv \mathbb{D}|\text{Stop} \) for some \( \mathbb{D} \in PCtx_\pi \).

A property of interest is whether standard reductions successfully terminate or not. Since reduction is nondeterministic, we define observations which test the existence of a successful sequence (may-convergence), and which test all reduction possibilities (should-convergence):

**Definition 2.8.** Let \( P \) be a \( \Pi_{\text{Stop}} \)-process. We say \( P \) is may-convergent (written \( P\downarrow \)), iff there is a successful process \( P' \) with \( P \xrightarrow{\text{sr}} P' \). We say \( P \) is should-convergent (written \( P\uparrow \)), iff, for all \( P' \): \( P \xrightarrow{\text{sr}} P' \) implies \( P\downarrow \). If \( P \) is not may-convergent, then \( P \) is must-divergent (written \( P\uparrow \)). If \( P \) is not should-convergent, then we say it is may-divergent (written \( P\uparrow \)).

**Example 2.9.** The process \( P := \nu x, y.((x(z).0 | \tau y.\text{Stop}) \) is may-convergent (\( P\downarrow \)) and should-convergent (\( P\uparrow \)), since \( P \xrightarrow{\text{sr}} 0 \) | \text{Stop} is the only standard reduction sequence for \( P \). The process \( P' := \nu x, y.((x(z).0 | \tau y.0) \) deterministically reduces to the silent process (i.e. \( P' \xrightarrow{\text{sr}} 0 \)), hence it is may-divergent (\( P'\uparrow \)) and even must-divergent (\( P'\uparrow \)). The process \( P'' := \nu x, y.((\tau y.0 | x(z).\text{Stop} | (x(z).0) \) shows that may-convergence and should-convergence are different. There are two standard-reduction possibilities for \( P'' \): we have \( P'' \xrightarrow{\text{sr}} \nu x, y.((\text{Stop} | (x(z).0) and \( P'' \xrightarrow{\text{sr}} \nu x, y.((x(z).\text{Stop}), \) where the first result is successful, and the second result is not successful. Hence, \( P'' \) is may-convergent but not should-convergent. It is also may-divergent, but not must-divergent.

Note that should-convergence implies may-convergence, and that must-divergence implies may-divergence.

**Definition 2.10.** For \( P, Q \in \Pi_{\text{Stop}} \) and observation \( \xi \in \{\downarrow, \uparrow, \uparrow\uparrow, \downarrow\uparrow\} \), we define \( P \leq_\xi Q \) iff \( PD \implies QD \). The \( \xi \)-contextual preorders and \( \xi \)-contextual equivalences are defined as

\[ P \leq_{c, \xi} Q \iff \forall C : C[P] \leq_\xi C[Q] \quad \text{and} \quad P \sim_{c, \xi} Q \iff P \leq_{c, \xi} Q \land Q \leq_{c, \xi} P \]

Contextual equivalence of \( \Pi_{\text{Stop}} \)-processes is defined as

\[ P \sim_{c, \downarrow} Q \iff P \sim_{c, \downarrow} Q \land P \sim_{c, \uparrow} Q. \]
Inspection of the reduction contexts and the $\xrightarrow{\mathit{in}}$-reduction shows:

**Lemma 2.11.** Let $P$ be a $\Pi_{\text{Stop}}$-process s.t. $\text{FN}(P) \subseteq \{x_1, \ldots, x_n\}$.

1. If $P \xrightarrow{\text{sr}, s} P'$ then $\nu x_1, \ldots, \nu x_n, P \xrightarrow{\text{sr}, s} \nu x_1, \ldots, x_n, P'$.
2. If $\nu x_1, \ldots, \nu x_n, P \xrightarrow{\text{sr}, s} P'$ then $P' \equiv \nu x_1, \ldots, x_n, P''$ and $P \xrightarrow{\text{sr}, s} P''$

**Lemma 2.12.** Let $\xi \in \{\downarrow, \uparrow, \downarrow, \uparrow\}$, $P, Q$ be $\Pi_{\text{Stop}}$-processes. Then $P \leq_{\xi, \xi} Q$ if, and only if $\forall C \in \mathcal{C}$ such that $C[P]$ and $C[Q]$ are closed: $C[P] \leq_{\xi} C[Q]$. 

**Proof.** One direction is trivial. For the other direction, we first consider $\xi = \downarrow$. Assume that $\forall C \in \mathcal{C}$ such that $C[P]$ and $C[Q]$ are closed: $C[P] \leq_{\downarrow} C[Q]$. Let $C$ be an arbitrary context such that $C[P] \downarrow$ and let $\text{FN}(C[P] \upharpoonright C[Q]) = \{x_1, \ldots, x_n\}$. Lemma 2.11 shows $\nu x_1, \ldots, x_n, C[P] \downarrow$. The precondition shows $\nu x_1, \ldots, x_n, C[Q] \downarrow$. Lemma 2.11 shows $C[Q] \downarrow$.

The case $\xi = \downarrow$ holds, since $\uparrow = \neg \downarrow$. If $\forall C \in \mathcal{C}$ such that $C[P]$ and $C[Q]$ are closed: $C[P] \leq_{\uparrow} C[Q]$, then this shows $C[Q] \leq_{\uparrow} C[P]$ which is equivalent to $C[P] \leq_{\xi, \xi} C[Q]$.

For $\xi = \uparrow$, assume that $\forall C \in \mathcal{C}$ such that $C[P]$ and $C[Q]$ are closed: $C[P] \leq_{\uparrow} C[Q]$. Let $C$ be an arbitrary context such that $C[P] \uparrow$ and let $\text{FN}(C[P] \upharpoonright C[Q]) = \{x_1, \ldots, x_n\}$. Lemma 2.11 and since adding or removing $\nu$-binders does not change success nor must-divergence we have $\nu x_1, \ldots, x_n, C[P] \uparrow$. Now the precondi-
tion shows $\nu x_1, \ldots, x_n, C[Q] \uparrow$, and Lemma 2.11 and since adding or removing $\nu$-binders does not change success nor must-divergence shows $C[Q] \uparrow$. Finally, the case $\xi = \downarrow$ follows by symmetry and since $P \uparrow \iff \neg(P \downarrow)$.

Since applying structural congruence is highly nondeterministic, and in order to facilitate reasoning on standard reduction sequences, we define a more restrictive use of the congruence laws which is still nondeterministic, but only applies the laws on the surfaces of the process.

**Definition 2.13.** Let $\xrightarrow{\mathit{dsc}}$ be the union of the following rules, where $\mathbb{D} \in \text{PCtxt}_\pi$:

- (assocl) $\mathbb{D}[P \downarrow (Q \uparrow R)] \rightarrow \mathbb{D}[P \downarrow (Q \uparrow R)]$ (assocr) $\mathbb{D}[P \downarrow (Q \uparrow R)] \rightarrow \mathbb{D}[P \downarrow (Q \uparrow R)]$
- (commute) $\mathbb{D}[P \downarrow Q] \rightarrow \mathbb{D}[Q \downarrow P]$ (replunf) $\mathbb{D}[P \downarrow \pi] \rightarrow \mathbb{D}[P \downarrow \pi]\Pi$
- (nuup1) $\mathbb{D}[\nu z. P \downarrow Q] \rightarrow \mathbb{D}[\nu z. (P \downarrow Q)]$, (nuup2) $\mathbb{D}[\nu z. \nu x. P] \rightarrow \mathbb{D}[\nu x. \nu z. P]$ if $z$ does not occur free in $Q$

Let $\xrightarrow{\mathit{dia}}$ be the closure of $\xrightarrow{\mathit{in}}$ by reduction contexts $\text{PCtxt}_\pi$ and let $\xrightarrow{\mathit{dsr}}$ be defined as the composition $\xrightarrow{\mathit{dsc}, \uparrow} \cdot \xrightarrow{\mathit{dia}} \cdot \xrightarrow{\mathit{dsc}, \downarrow}$.

We omit the proof of the following equivalences, but it can be constructed analogous to the proof given in [23], for barbed may- and should-testing. The theorem allows us to restrict standard reduction to $\xrightarrow{\mathit{dsr}}$-reduction when reasoning on reduction sequences that witness may-convergence or may-divergence, resp.

**Theorem 2.14.** For all processes $P \in \Pi_{\text{Stop}}$ the following holds:

1. $P \downarrow$ iff $P \xrightarrow{\mathit{dsr}} \mathbb{D}[\text{Stop}]$.
2. $P \uparrow$ iff $\exists P'$ such that $P \xrightarrow{\mathit{dsr}} P'$ and $P' \uparrow$. 


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\[ P \in \text{Proc}_{\text{CH}} := (P_1 \mid P_2) \mid \leftarrow e \mid \nu x.P \mid x \cdot m \cdot e \mid x : m - \mid x : e \]

\[ e \in \text{Expr}_{\text{CH}} := x \mid \lambda x.e \mid (e_1 \mid e_2) \mid \text{seq} e_1 e_2 \mid c e_1 \ldots e_{\text{alt}(c)} \]

\[ \left\{ \begin{array}{l}
| \text{letrec}\ x_1 = e_1, \ldots, x_n = e_n \text{ in } e \mid m |
| \text{case } e \text{ of } (e_1 \mid x_1 \ldots x_{\text{alt}(e_1)} \rightarrow e_1) \ldots (e_T | T | x_1 \ldots x_{\text{alt}(e_T)} \rightarrow e | T_1) |
\end{array} \right. \]

\[ m \in \text{MExpr}_{\text{CH}} := \text{return } e \mid e \gg e' \mid \text{forkIO } e \mid \text{takeMVar } e \mid \text{newMVar } e \mid \text{putMVar } e e' \]

\[ t \in \text{Typ}_{\text{CH}} := \text{IO} t \mid (T \mid t_1 \ldots t_n) \mid \text{MVar } t \mid t_1 \rightarrow t_2 \]

Fig. 4. Syntax of expressions, processes, and types of CH

\[ P_1 \mid P_2 \equiv P_3 \mid P_1 \mid (P_2 \mid P_3) \]
\[ (P_1 \mid P_2) \mid P_3 \equiv P_1 \mid (P_2 \mid P_3) \]
\[ (\nu x.P) \mid P_2 \equiv \nu x.(P_1 \mid P_2), x \notin \text{FV}(P_2) \]
\[ \nu x_1, \nu x_2.P \equiv \nu x_2, \nu x_1.P \]
\[ P_1 \equiv P_2 \text{ if } P_1 =_a P_2 \]

Fig. 5. Structural congruence of CH

Fig. 6. Several context classes

Monadic Computations:

\[ \text{mon} \quad \leftarrow \mathbb{M}[\text{return } e_1 \gg e_2] \xrightarrow{sr} \leftarrow \mathbb{M}[e_2 \ e_1] \]
\[ \text{tnvar} \quad \leftarrow \mathbb{M}[\text{takeMVar } x] \mid x : m e \xrightarrow{sr} \leftarrow \mathbb{M}[\text{return } e] \mid x : m - \]
\[ \text{pmvar} \quad \leftarrow \mathbb{M}[\text{putMVar } x e] \mid x : m - \quad \xrightarrow{sr} \quad \leftarrow \mathbb{M}[\text{return } ()] \mid x : m e \]
\[ \text{nnvar} \quad \leftarrow \mathbb{M}[\text{newMVar } e] \xrightarrow{sr} \nu x.(\leftarrow \mathbb{M}[\text{return } x] \mid x : m e) \]
\[ \text{fork} \quad \leftarrow \mathbb{M}[\text{forkIO } e] \xrightarrow{sr} \leftarrow \mathbb{M}[\text{return } t] \mid t \xrightarrow{e} \]

Functional Evaluation:

\[ \text{cpce} \quad \leftarrow \mathbb{M}[\text{F}[x]] \mid x : e \rightarrow \leftarrow \mathbb{M}[\text{F}[e]] \mid x : e \]
\[ \text{mbk} \quad \leftarrow \mathbb{M}[\text{letrec } x_1 = e_1, \ldots, x_n = e_n \text{ in } e] \]
\[ \xrightarrow{sr} \nu x_1 \ldots x_n. (\leftarrow \mathbb{M}[\text{F}[e]] \mid x_1 = e_1 \ldots \mid x_n = e_n) \]
\[ \text{beta} \quad \leftarrow \mathbb{M}[\text{F}[((\lambda x.e_1) e_2)] \xrightarrow{sr} \leftarrow \mathbb{M}[\text{F}[e_1][e_2/x]] \]
\[ \text{case} \quad \leftarrow \mathbb{M}[\text{F}[\text{case } r (c e_1 \ldots e_n) \ldots (c y_1 \ldots y_n \rightarrow e)\ldots]] \xrightarrow{sr} \leftarrow \mathbb{M}[\text{F}[e/c][e_1/y_1, \ldots, e_n/y_n]] \]
\[ \text{seq} \quad \leftarrow \mathbb{M}[\text{F}[\text{seq } v e]] \xrightarrow{sr} \leftarrow \mathbb{M}[\text{F}[e]] \quad \text{where } v \text{ is a functional value} \]

Closure: If \( P_1 \equiv \mathbb{D}[P_1] \), \( P_2 \equiv \mathbb{D}[P_2] \), and \( P_1 \xrightarrow{sr} P_2 \) then \( P_1 \xrightarrow{sr} P_2 \).

Capture avoidance: We assume capture avoiding reduction for all reduction rules.

Fig. 7. Standard reduction rules of CH (call-by-name-version)
3 The Process Calculus $CH$

We introduce the calculus $CH$ (which is a variant of the calculus $CHF$ in [25,26]) which models a core language of Concurrent Haskell [18]. We assume a partitioned set of data constructors $c$ where each family represents a type $T$. The data constructors of type $T$ are $c_{T,1}, \ldots, c_{T,[T]}$ where each $c_{T,i}$ has an arity $\text{ar}(c_{T,i}) \geq 0$. We assume that there is a type $\emptyset$ with data constructor $\emptyset$, a type $\text{Bool}$ with constructors $\text{True}$, $\text{False}$, a type $\text{List}$ with constructors $\text{Nil}$ and $\text{::}$ (written infix as in Haskell), and a type $\text{Pair}$ with a constructor $(,)$ written as $(a,b)$. The syntax of the calculus $CH$ has processes $P \in \text{Proc}_{CH}$ on the top-level which may have expressions $e \in \text{Expr}_{CH}$ as subterms. The syntax of both is shown in Fig. 4 where $u, w, x, y, z$ denote variables from a countably-infinite set of variables $\text{Var}$. As in the $\text{HStop}$-calculus, parallel processes are formed by parallel composition “$|$”. The $\nu$-binder restricts the scope of a variable. A concurrent thread $\nu e$ evaluates the expression $e$. In a process there is (at most one) unique distinguished thread, called the main thread written as $\text{main } e$. MVars are mutable variables which are empty or filled. A thread blocks if it wants to fill a filled MVar $x \text{m} e$ or empty an empty MVar $x \text{m} \gets$. The variable $x$ is called the name of the MVar. Bindings $x = e$ model the global heap, where $x$ is called a binding variable. If variable $x$ is a name of an MVar or a binding variable, then $x$ is called an introduced variable. An introduced variable is visible to the whole process unless its scope is restricted by a $\nu$-binder, i.e. in $Q\nu x.P$ the scope of introduced variable $x$ is process $P$. A process is well-formed, if all introduced variables are pairwise distinct and there exists at most one main thread $\text{main } e$.

Expressions $\text{Expr}_{CH}$ consist of functional expressions and monadic expressions $\text{MExpr}_{CH}$ that model IO-operations. Functional expressions are variables, abstractions $\lambda x.e$, applications $(e_1 e_2)$, seq-expressions $(\text{seq } e_1 e_2)$, constructor applications $(c e_1 \ldots e_{\text{ar}(c)})$, letrec-expressions $(\text{letrec } x_1 = e_1, \ldots, x_n = e_n \text{ in } e)$, and case$_T$-expressions for every type $T$. We abbreviate case-expressions as case$_T$\ $e$ of alts where alts are the case-alternatives. The case-alternatives have exactly one alternative $(c_{T,i} x_1 \ldots x_{\text{ar}(c_{T,i})} \rightarrow e_i)$ for every constructor $c_{T,i}$ of type $T$, where $x_1, \ldots, x_{\text{ar}(c_{T,i})}$ (occurring in the pattern $c_{T,i} x_1 \ldots x_{\text{ar}(c_{T,i})}$) are pairwise distinct variables that become bound with scope $e_i$. We omit the type index $T$ in case$_T$ if it is clear from the context. In $(\text{letrec } x_1 = e_1, \ldots, x_n = e_n \text{ in } e)$ the variables $x_1, \ldots, x_n$ are pairwise distinct and the bindings $x_i = e_i$ are recursive, i.e. the scope of $x_i$ is $e_1, \ldots, e_n$ and $e$. Monadic operators $\text{newMVar}$, $\text{takeMVar}$, and $\text{putMVar}$ are used to create, to empty and to fill MVars, the “bind”-operator $\gg=$ implements the sequential composition of IO-operations, the forkIO-operator performs thread creation, and return lifts expressions to monadic expressions.

Functional values are abstractions and constructor applications. If a monadic expression is of the form $(\text{newMVar } e)$, $(\text{takeMVar } e)$, $(\text{return } e)$, $(e_1 \gg= e_2)$, $(\text{forkIO } e)$, or $(\text{putMVar } e_1 e_2)$, then it is a monadic value. A value is either a functional or a monadic value.
Abstractions, letrec-expressions, case-alternatives, and $\nu x.P$ introduce variable binders and thus this induces free and bound variables, $\alpha$-renaming, and $\alpha$-equivalence $=_{\alpha}$. Let $FV(P)$ ($FV(e)$, resp.) be the free variables of process $P$ (expression $e$, resp.). We assume the distinct variable convention to hold: free variables are distinct from bound variables, and bound variables are pairwise distinct. We assume that reductions perform $\alpha$-renaming to obey this convention. Structural congruence $\equiv$ of CH-processes is the least congruence satisfying the laws in Fig. 5. It allows to treat parallel composition as an associative-commutative operator, to shift $\nu$-binders to the top-level of processes and to $\alpha$-rename processes.

We assume that expressions and processes are well-typed according to a standard monomorphic type system: Since the typing rules are standard, we omit them, but explain the syntax of types (see Fig. 4): $(\text{IO} t)$ stands for a monadic action with return type $t$, $(\text{MVar} t)$ stands for an MVar with content type $t$, and $t_1 \rightarrow t_2$ is a function type. We treat constructors like overloaded constants to use them in a polymorphic way.

We introduce a call-by-name small-step reduction for CH. This operational semantics can be shown to be equivalent to (a more realistic) call-by-need semantics. The proof is a copy of the proof given in [25] for the calculus CHF. However, the equivalence of the reduction strategies is not important or needed for the current paper. That is why we do not include it.

A context is a process or an expression with a (typed) hole $[\cdot]$. We introduce some classes of contexts in Fig. 6. On the process level there are process contexts $PCtxt_{CH}$, on expressions first monadic contexts $MCtxt_{CH}$ are used to find the next to-be-evaluated monadic action in a sequence of actions. For the evaluation of (purely functional) expressions, usual (call-by-name) expression evaluation contexts $E \in ECtxt_{CH}$ are used, and to enforce the evaluation of the (first) argument of the monadic operators $\text{takeMVar}$ and $\text{putMVar}$ the class of forcing contexts $F \in FCtxt_{CH}$ is used.

**Definition 3.1.** The standard reduction $\rightarrow_{sr}$ is defined by the rules and the closure in Fig. 7. We permit standard reduction only for well-formed processes which are not successful.

Functional evaluation includes $\beta$-reduction (beta), rule (cpce) for copying shared bindings into needed positions, rules (case) and (seq) to evaluate case- and seq-expressions, and rule (mkbinds) to move letrec-bindings into the global set of shared bindings. For monadic computations, rule (lunit) implements the monadic evaluation by applying the first monad law. Rules (nmvar), (tmvar), and (pmvar) handle the MVar creation and access. A $\text{takeMVar}$-operation can only be performed on a filled MVar, and a $\text{putMVar}$-operation needs an empty MVar. Rule (fork) spawns a new concurrent thread. A concurrent thread finishes its computation if it is of the form $\leftarrow \text{return } e$ (where $e$ is of type $()$).

A CH-process $P$ is successful if $P \equiv \nu x_1.\ldots.\nu x_n. (\text{main return } e | P')$ and $P$ is well-formed. These are the desired results of standard reduction sequences. The successful processes capture the behavior that termination of the main-thread implies termination of the whole program.
Definition 3.2. Let $P$ be a CH-process. Process $P$ may-converges (written as $P\downarrow$), iff $P$ is well-formed and $\exists P^\prime: P \xrightarrow{st,e} P^\prime \land P^\prime$ successful. If $P\not\downarrow$ does not hold, then $P$ must-diverges written as $P\nabla$. Process $P$ should-converges (written as $P\parallel$), iff $P$ is well-formed and $\forall P^\prime: P \xrightarrow{st,e} P^\prime \implies P\not\downarrow$. If $P$ is not should-convergent, then we say $P$ may-diverges written as $P\nabla$.

Note that a process $P$ is may-divergent iff there is a finite reduction sequence $P \xrightarrow{st,e} P^\prime$ such that $P^\prime\parallel$. Definition 3.2 implies that non-well-formed processes are always may-divergent, since they are irreducible and never successful.

Definition 3.3. Contextual approximation $\leq_c$ and contextual equivalence $\sim_c$ on CH-processes are defined as $\leq_c := \leq_{c,\downarrow} \cap \leq_{c,\parallel}$ and $\sim_c := \leq_c \cap \geq_c$ where

- $P_1 \leq_{c,\downarrow} P_2$ iff $\forall \mathbb{D} \in PCtx_{CH}: \mathbb{D}[P_1] \downarrow \implies \mathbb{D}[P_2] \downarrow$
- $P_1 \leq_{c,\parallel} P_2$ iff $\forall \mathbb{D} \in PCtx_{CH}: \mathbb{D}[P_1] \parallel \implies \mathbb{D}[P_2] \parallel$

For CH-expressions, let $e_1 \leq_c e_2$ iff for all process-contexts $C$ with a hole at expression position: $C[e_1] \leq_c C[e_2]$ and $e_1 \sim_c e_2$ iff $e_1 \leq_c e_2 \land e_2 \leq_c e_1$.

The following equivalence will help to prove properties of our translation.

Lemma 3.4. The relations in Definition 3.3 are unchanged, if we add a closedness restriction as follows. Let $\xi \in \{\downarrow, \parallel\}$, then $P_1 \not\downarrow, \xi P_2$ iff $\forall \mathbb{D} \in PCtx_{CH}$ such that $\mathbb{D}[P_1], \mathbb{D}[P_2]$ are closed: $\mathbb{D}[P_1]_{\xi} \implies \mathbb{D}[P_2]_{\xi}$.

Proof. One direction is obvious. For the other direction, let $\xi \in \{\downarrow, \parallel\}$ and assume that for all $\mathbb{D}$ such that $\mathbb{D}[P_1], \mathbb{D}[P_2]$ are closed: $\mathbb{D}[P_1]_{\xi} \implies \mathbb{D}[P_2]_{\xi}$. Assume that $\mathbb{D}[P_1]_{\xi}$ and $FV(\mathbb{D}[P_1]) \cup FV(\mathbb{D}[P_2]) = \{x_1, \ldots, x_n\}$. Since reductions are applicable with or without $\nu$-binders on the top, we have $\nu x_1, \ldots, x_n, \mathbb{D}[P_1]_{\xi}$ and by the precondition $\nu x_1, \ldots, x_n, \mathbb{D}[\cdot]_{\xi}$ is a PCtx-context. From $\nu x_1, \ldots, x_n, \mathbb{D}[P_2]_{\xi}$ also $\mathbb{D}[P_2]_{\xi}$ follows, since reductions are applicable with or without $\nu$-binders on the top. This shows $P_1 \leq_{c,\xi} P_2$.

Proposition 3.5. Let $P_1, P_2$ be well-formed and $P_1 \equiv P_2$. Then $P_1 \sim_c P_2$.

A program transformation $\eta \subseteq (Proc_{CH} \times Proc_{CH})$ is correct iff $P_1 \eta P_2 \implies P_1 \sim_c P_2$. In Fig. 8 we define garbage collection (gc) and deterministic variants of (tmvar) and (pmvar).

The following proposition holds, since correctness of transformations can be transferred from the calculus CHF by an embedding $i: CH \rightarrow CHF$, such that $i(P) \sim_{c,CHF} i(P^\prime)$ implies $P \sim_{c,CH} P^\prime$. See Appendix C.3 for the full proof.

Proposition 3.6. The rules (lunit), (nmvar), (fork), (cpce), (mkbinds), (beta), (case), (seq), (gc), (dtmvar), and (dpmvar) are correct program transformations.
Embedding the Pi-Calculus into CH

4 The Translation \( \tau_0 \) with Private Names

We present a translation that uses private names (i.e. new MVars) which uses similar ideas as the translation of the pi-calculus into an asynchronous pi-calculus in [3], which was an early example of using private names within a translation. Later in Sect. 5 we discuss translations and investigations on those translations which do not use private names.

We define a translation \( \tau_0 \) mapping \( \Pi_{\mathrm{Stop}} \)-processes to \( CH \)-processes. It uses the data type \( \text{Channel} \) (with data constructor \( \text{Channel} \)), defined in Haskell-syntax as

\[
\text{data Channel} = \text{Channel} (\text{MVar} (\text{Channel}, (\text{MVar} ()))).
\]

The type \( \text{Channel} \) is a recursive data type, which can be initialized with a \( \perp \)-expression. In the following, we abbreviate the expression \( \text{case} \ e \ \text{of} \ (\text{Channel} \ m \ \rightarrow \ m) \) as \( \text{unchan} \ e \) and we use \( a \gg b \) as abbreviation for \( a \\gg (\lambda x. b) \) and also use Haskell’s do-notation as abbreviation, where

\[
\begin{align*}
\text{do} \{ x \leftarrow e_1; e_2 \} &= e_1 \\gg (\lambda x. \text{do} \{ e_2 \}) \\
\text{do} \{(x,y) \leftarrow e_1; e_2\} &= e_1 \\gg (\lambda z. \text{casePair} z \ \text{of} \ (x,y) \rightarrow (\text{do} \{ e_2 \})) \\
\text{do} \{ e_1; e_2 \} &= e_1 \\gg (\text{do} \{ e_2 \}) \\
\text{do} \{ e \} &= e
\end{align*}
\]

As a further abbreviation, we write \( y \leftarrow \text{newEmptyMVar} \) inside a do-block to abbreviate the sequence \( y \leftarrow \text{newMVar} \ \perp; \text{takeMVar} \ y \), where \( \perp \) is a must-divergent expression, for instance \( \text{letrec} x = x \ \text{in} \ x \).

The translation from the \( \Pi_{\mathrm{Stop}} \)-calculus is done by using one MVar per channel which contains a pair consisting of the (translated) name of the channel and a further MVar used for the synchronization. This MVar is private, such that only the sender and the receiver know it. Privacy is established by the sender: it creates a new MVar for every send operation.

**Fig. 8.** Garbage Collection and Deterministic Take und Put
Fig. 9. Translations $\tau_0$ and $\tau$  

Message $y$ is sent over channel $x$ by sending a pair $(y, \text{check})$ where $\text{check}$ is a MVar containing $(\cdot)$. The receiver waits (black square) for a message $(y, \text{check})$ by the sender. After sending the message, the sender waits until $\text{check}$ is emptied, and the receiver acknowledges by emptying the MVar $\text{check}$.

**Definition 4.1.** We define the translation $\tau_0$ and its inner translation $\tau$ from the $\Pi_{\text{Stop}}$-calculus into the $\mathcal{CH}$-calculus in Fig. 9 as follows. For contexts, the translations are the same where the context hole is treated like a constant and translated as $\tau([\cdot]) = [\cdot]$.

The translation $\tau_0$ generates a main-thread and an MVar $\text{stop}$. The main thread is then waiting for the MVar $\text{stop}$ to be emptied. The inner translation $\tau$ translates the constructs and constants of the $\Pi_{\text{Stop}}$-calculus into $\mathcal{CH}$-expressions.

**Remark 4.2.** Except for the main-thread, the translation $\tau_0$ generates a valid Concurrent Haskell-program, i.e. if we write $\tau_0(P) = \begin{equation*} e \end{equation*}$ as $\begin{equation*} \text{main} = e, \end{equation*}$ we can execute the translation in the Haskell-Interpreter.

**4.1 Properties of the Translation $\tau_0$**

In this section we define properties of translations and motivate and present the plan for proving the results for the translations $\tau_0$ and $\tau$, which will be in Sect. 4.2 (and details also in Appendix A).

For the following definition of translation $\tau$ being compositional, adequate, or fully abstract, we adopt the view of the translation $\tau$ to be a translation from $\Pi_{\text{Stop}}$ into the CH-language with a special initial evaluation context $C^\tau_{\text{out}}$. 
Definition 4.3. The context $C^\tau_{\text{out}}$ for translation $\tau_0$ is defined as

$$C^\tau_{\text{out}} = \nu f, \text{stop.} \max \{ \text{stop} \leftarrow \text{newMVar ()}; \forkIO []; \putMVar \text{stop} () \}$$

We define variants of may- and should-convergence of expressions $e$ within $C^\tau_{\text{out}}$ in $CH$:

$$e\downarrow_0 \text{ iff } C^\tau_{\text{out}}[e] \downarrow \text{ and } e\uparrow_0 \text{ iff } C^\tau_{\text{out}}[e] \uparrow.$$  

We define the relations $\leq_{c,\tau_0}$ and $\sim_{c,\tau_0}$ on $CH$-expressions:

$$e_1 \leq_{c,\tau_0} e_2 \text{ iff } \forall C : \text{if } FV(C[e_1], C[e_2]) \subseteq \{ \text{stop} \}, \text{then } C[e_1]_0 \Rightarrow C[e_2]_0 \text{ and } C[e_1]_0 \Rightarrow C[e_2]_0$$

$$e_1 \sim_{c,\tau_0} e_2 \text{ iff } e_1 \leq_{c,\tau_0} e_2 \text{ and } e_1 \leq_{c,\tau_0} e_2.$$  

Note that $\leq_{c,CH}$ is a subset of $\leq_{c,\tau_0}$, and hence also $\sim_{c,CH}$ is a subset of $\sim_{c,\tau_0}$, and thus we often can use the more general relations for reasoning and in particular can reuse results from $CH$.

Definition 4.4. Let $\Pi_{\text{Stop}}$ be the contexts of $\Pi_{\text{Stop}}$. We define the following properties for $\tau_0$ and $\tau$ (see [30,32]). For open processes $P, P'$, we say that translation $\tau$ is

compositional upto $\{ \downarrow_0, \uparrow_0 \}$ iff for all $P \in \Pi_{\text{Stop}}$, all $C \in \Pi_{\text{Stop}}, C$, and all $\xi \in \{ \downarrow_0, \uparrow_0 \}$: if $FV(C[P]) \subseteq \{ \text{stop} \}$, then $\tau(C[P])\xi \iff \tau(C)[\tau(P)]\xi$.

adequate iff for all processes $P, P' \in \Pi_{\text{Stop}}$: $\tau(P) \leq_{c} \tau(P') \iff P \leq_{c} P'$, and

fully abstract iff for all processes $P, P' \in \Pi_{\text{Stop}}$: $P \leq_{c} P' \iff \tau(P) \leq_{c,\tau_0} \tau(P')$.

Convergence equivalence of the translation $\tau_0$ for may- and should-convergence holds. For readability and space reasons the proofs are omitted, but given in a technical appendix. In Appendix A we thus show the following two propositions:

Proposition 4.5. Let $P \in \Pi_{\text{Stop}}$ be closed. Then $\tau_0$ is convergence-equivalent for $\downarrow$, i.e. $P \downarrow$ is equivalent to $\tau_0(P) \downarrow$. This also implies that $P \parallel$ is equivalent to $\tau_0(P) \parallel$.

Proposition 4.6. Let $P \in \Pi_{\text{Stop}}$ be closed. Then $\tau_0$ is convergence-equivalent for $\downarrow$, i.e. $P \downarrow$ is equivalent to $\tau_0(P) \downarrow$.

We informally describe the main steps of the proof. Both propositions have an “if” and an “only-if” part, i.e. to prove them, one has to show four claims:

1. $\tau_0$ preserves may-convergence, i.e. for closed $P \in \Pi_{\text{Stop}}$: $P \downarrow \Rightarrow \tau_0(P) \downarrow$.
2. $\tau_0$ reflects may-convergence, i.e. for closed $P \in \Pi_{\text{Stop}}$: $\tau_0(P) \downarrow \Rightarrow P \downarrow$.
3. $\tau_0$ preserves must-convergence, i.e. for closed $P \in \Pi_{\text{Stop}}$: $P \downarrow \Rightarrow \tau_0(P) \downarrow$.
4. $\tau_0$ reflects must-convergence, i.e. for closed $P \in \Pi_{\text{Stop}}$: $\tau_0(P) \downarrow \Rightarrow P \downarrow$.  


Actually, for the latter two parts, we show reflection and preservation of may-divergence, i.e.:

3'. $\tau_0$ reflects may-divergence, i.e. for closed $P \in \Pi_{\text{Stop}}$: $\tau_0(P)^{\uparrow} \implies P^{\uparrow}$.

4'. $\tau_0$ preserves may-divergence, i.e. for closed $P \in \Pi_{\text{Stop}}$: $P^{\uparrow} \implies \tau_0(P)^{\uparrow}$.

Note that claim 3 is equivalent to 3' and claim 4 is equivalent to claim 4'.

The proof technique for showing parts 1, 2, 3', 4' is to investigate properties of reduction sequences (those that end in a successful process and those that end in a must-divergent process) and to (inductively) apply appropriate (and permitted) rearrangements of reduction sequences and correct program transformations, i.e. those listed in Proposition 3.6.

For the given reduction sequences in the $\Pi_{\text{Stop}}$-calculus (giving evidence that $P_\downarrow$, or $P^{\uparrow}$ resp. holds) for proving part 1 and 4', we assume that they are given as steps of $d_{sr}$-reductions (Definition 2.13). This simplifies the proof and is correct due to Theorem 2.14.

A further technical detail is that we use another translation $\sigma$ (and $\sigma_0$) which translates several $\pi$-process components directly into $CH$-process components (for instance, parallel composition is directly translated into parallel composition), instead of translating them into code which generates the components (as $\tau$ does). An intermediate step in the proof is to show equivalence of $\sigma$ and $\tau$ w.r.t. contextual equivalence.

For proving the reflection parts, i.e. part 2 and part 3, the following diagrams sketch the overall idea of the induction proof, which mainly is on the number $n$ of given reductions.

For part 2 the diagram is

$$P \xrightarrow{sr,\ast} P_1 \xrightarrow{\sigma_0} \sigma_0(P_1)(\text{succ.}) \xleftarrow{\tau_{\text{sr}}(\ast),\ast} Q_1(\text{succ.})$$

For part 3, the diagram is

$$P_1 \xrightarrow{sr,\ast} P_1 \xrightarrow{\sigma_0} \sigma_0(P_1) \xleftarrow{\tau_{\text{sr}}(\ast),\ast} Q_1 \xrightarrow{sr,\ast}$$

The difference of both parts is the induction base: For part 2, the processes are successful, while for part 3, the processes have to be must-divergent. Both diagrams also illustrate the use of correct program transformations to obtain a "reordered" sequence of reductions and transformation for $\sigma_0(P)$ such that it can be back-translated into a sequence of $sr$-reductions in the $\Pi_{\text{Stop}}$-calculus.

### 4.2 Main Results for the Translation $\tau_0$

We show the main result that the translation is adequate (Theorem 4.10). The interpretation of this result is that the pi-calculus with the concurrent semantics
is semantically represented within $CH$. This result is on a more abstract level, since it is based on the property whether the programs (in all contexts) produce values or may run into failure, or get stuck; or not. Since the pi-calculus does not have a notion of values, like numbers or lists, also the translated processes cannot be compared w.r.t. values other than a single form of value.

The translation $\tau_0$ is not fully abstract (Theorem 4.11), which is rather natural, since it only means that it is mapped into a subset of the expressions and that this is a proper subset w.r.t. the semantics.

First we show a simple form of a context lemma:

**Lemma 4.7.** Let $e, e'$ be $CH$-expressions, where the only free variable in $e, e'$ is stop. Then $C_{\text{out}}^\tau[e] \leq C_{\text{out}}^\tau[e']$ iff $C_{\text{out}}^\tau[e]\downarrow \implies C_{\text{out}}^\tau[e']\downarrow$ and $C_{\text{out}}^\tau[e]\uparrow \implies C_{\text{out}}^\tau[e']\uparrow$.

**Proof.** One direction is obvious by using the empty context in the $\leq_c$-definition. For the other direction, assume $C_{\text{out}}^\tau[e]\downarrow \implies C_{\text{out}}^\tau[e']\downarrow$ and $C_{\text{out}}^\tau[e]\uparrow \implies C_{\text{out}}^\tau[e']\uparrow$. There are two cases:

1. Let $D$ be a process-context such that $D[C_{\text{out}}^\tau[e]]$ and $D[C_{\text{out}}^\tau[e']]$ are closed, and $D[C_{\text{out}}^\tau[e]]\downarrow$. Then we have to show that $D[C_{\text{out}}^\tau[e']][\downarrow]$. Due to closedness, $D[C_{\text{out}}^\tau[e]] \equiv P_\downarrow C_{\text{out}}^\tau[e]$ and $D[C_{\text{out}}^\tau[e']] \equiv P_\downarrow C_{\text{out}}^\tau[e']$ for some closed $CH$-process $P_\downarrow$. Due to closedness, there is no interference between $P_\downarrow$ and $C_{\text{out}}^\tau[e']$, or $C_{\text{out}}^\tau[e]$, resp. Hence $C_{\text{out}}^\tau[e]\downarrow \implies C_{\text{out}}^\tau[e']\downarrow$, and thus $D[C_{\text{out}}^\tau[e']][\downarrow]$.

2. Assume that $D[C_{\text{out}}^\tau[e']][\uparrow]$. We have to show that $D[C_{\text{out}}^\tau[e'][\uparrow]$. Due to closedness, we have $D[C_{\text{out}}^\tau[e]] \equiv P_\uparrow C_{\text{out}}^\tau[e]$ and $D[C_{\text{out}}^\tau[e']] \equiv P_\uparrow C_{\text{out}}^\tau[e']$ and the same arguments as in item 1 show the claim.

**Proposition 4.8.** The translation $\tau$ is compositional w.r.t. $\{\downarrow_0, \uparrow_0\}$.

**Proof.** This follows by checking whether the single cases of the translation $\tau$ are independent of the surrounding context, and translate every level independently.

**Theorem 4.9.** Let $P \in \Pi_{\text{Stop}}$ be closed. Then $\tau_0$ is convergence-equivalent for $\downarrow$ and $\uparrow$, i.e. $P\downarrow$ is equivalent to $\tau_0(P)\downarrow$, and $P\uparrow$ is equivalent to $\tau_0(P)\uparrow$. This also shows convergence-equivalence of $\tau$ w.r.t. $\downarrow_0, \uparrow_0$, i.e. For closed $P \in \Pi_{\text{Stop}} : P\downarrow \iff \tau(P)\downarrow_0$ and $P\uparrow \iff \tau(P)\uparrow_0$.

**Proof.** This follows from Proposition 4.5 for may-convergence and from Proposition 4.6 for should-convergence.

We show in the following that the translation $\tau$ transports $\Pi_{\text{Stop}}$-processes into $CH$, such that adequacy holds. This is a rather strong statement: It shows that the translated processes also mimic the behavior of the original $\Pi_{\text{Stop}}$-processes when plugged into contexts in a correct way. If the translated open processes cannot be distinguished by $\leq_c, \tau_0$, i.e. there is no test that detects a difference where may and should-convergence are the observables, then the original processes are equivalent in the pi-calculus.
However, this open translation is not fully abstract, which means that there are $\text{CH}$-contexts that can see and exploit too much of the details of the translation.

We state and prove the main result of our paper.

**Theorem 4.10.** The translation $\tau$ is adequate.

*Proof.* We prove the adequacy-property for the preorder $\leq_{c,\tau_0}$. The property for the contextual equivalence $\sim_{c,\tau_0}$ and $\sim_c$ then follows by symmetry. Let $P, P'$ be $\Pi_{\text{Stop}}$-processes, such that $\tau(P) \leq_{c,\tau_0} \tau(P')$. We show that $P \leq_c P'$. Now we use the equivalence in Lemma 3.4 and restrict considerations to closed $C[P], C[P']$ below: Let $C$ be a context in $\Pi_{\text{Stop}}$, such that $C[P], C[P']$ are closed and $C[P]\downarrow$. Then $\tau_0(C[P]) = C^\text{out}_\tau[\tau(C[P])]$. Closed convergence equivalence shows that $C^\text{out}_\tau[\tau(C[P])]\downarrow$, which is the same as $C^\text{out}_\tau[\tau(C)[\tau(P)]]\downarrow$ by Proposition 4.8. The assumption $\tau(P) \leq_{c,\tau_0} \tau(P')$ implies $C^\text{out}_\tau[\tau(C)[\tau(P)]]\downarrow$, which again is the same as $C^\text{out}_\tau[\tau(C[P])]\downarrow$ using Proposition 4.8. Again, closed convergence equivalence implies $C[P]\downarrow$. The same arguments holds for $\downarrow$ instead of $\downarrow$. The computation can be done for every context $C$ that satisfies the conditions.

In summary, we obtain $P \leq_c P'$.

**Theorem 4.11.** The translation $\tau$ is not fully abstract.

*Proof.* This holds, since an open translation can be closed in $\text{CH}$ by a context without initializing the $\nu$-bound MVars. For $P = \bar{x}(y).\text{Stop}\,\bar{x}(z).\text{Stop}$, we have $P \sim_c \text{Stop}$ in the $\pi$-calculus (see Example D.2), but $\tau(P) \not\sim_{c,0} \tau(\text{Stop})$ in $\text{CH}$; let $\mathcal{D}$ be a process context that does not initialize the MVars for $x$ (as the translation does). Then $\mathcal{D}[\tau(P)]\uparrow_0$, but $\mathcal{D}[\tau(\text{Stop})]\downarrow_0$.

However, restricted to closed processes, full abstraction holds:

**Theorem 4.12.** For closed $P_1, P_2 \in \Pi_{\text{Stop}}$, the equivalence $P_1 \leq_c P_2 \iff \tau(P_1) \leq_{c,\tau_0} \tau(P_2)$ holds.

*Proof.* For closed $P, P'$, the implication $P \leq_c P' \implies \tau(P_1) \leq_{c,\tau_0} \tau(P_2)$ follows from Lemma 4.7 since $\tau_0$ produces closed processes that are in context $C^\text{out}_\tau$. The other direction follows from Theorem 4.10.

## 5 Translations with Global Names

In this section, we discuss translations that do not use private names, but only global names. We report on an automated tool that we implemented and that searches for those translations trying to refute their correctness. We only consider the aspect of how to encode the synchronous message passing of the $\pi$-calculus, the other aspects (encoding parallel composition, replication and the Stop-constant) are not discussed, where we assume that they are encoded as before. We also keep the main idea to translate a channel of the $\pi$-calculus into $\text{CH}$: we represent a $\pi$-channel in $\text{CH}$ as an object of a user-defined data type $\text{Channel}$ that consists of...
1. an MVar for transferring the message (which again is a Channel),
2. additional MVars for implementing a correct synchronization mechanism.

In the translation $\tau$ in the previous section, we used a private MVar (which was created by the sender for every communication action and transferred together with the message). In this section we investigate translations where this mechanism is replaced by one or several public MVars, which are created once together with the channel object. To restrict the search space for translations, only the synchronization mechanism of MVars (by emptying and filling them) is used, but we forbid to transfer specific data (like numbers etc.). Hence, we restrict these MVars (which are called check-MVars from now on) to be of type $\text{MVar} ()$. Such MVars are comparable to binary semaphores, where filling and emptying correspond to signal and wait.

In summary, we now analyze translations of $\pi$-calculus channels into a CH-data type $\text{Channel}$ where the data type definition in Haskell is of the form

\[
\text{data Channel} = \text{Chan } (\text{MVar} \text{ Channel}) (\text{MVar} () \ldots (\text{MVar} () \ldots n \text{ times})
\]

A channel $x$ of the $\pi$-calculus is then represented as a $CH$-binding

\[
x = \text{Chan } \text{content check}_1 \ldots \text{check}_n
\]

where $\text{content}$, $\text{check}_1$, $\ldots$, $\text{check}_n$ are appropriately initialized (i.e. empty) MVars. The MVars are not private (but public (or global), since all processes which know the name $x$ have access to the $n + 1$ components of the channel.

After fixing this representation of a $\pi$-channel in $CH$, the task is to translate the input- and output actions $x(y)$, $xz$ as $CH$-programs such that the interaction reduction is performed correct and synchronously\footnote{We omit the translation of the $\nu$-operator at this moment, but clearly, it has to construct and initialize the appropriate MVars. In Definition 5.2 the full translation will be given.}.

Let us call the translation of $x(y)$, the receiver program (the receiver, for short), and the translation of $xz$ the sender program (the sender, for short). To simplify the analysis, we restrict the allowed operations of the sender and the receiver, and thus allow only the following operations:

**putS:** The sender puts its message into the contents-MVar of the channel. This operation occurs exactly once in the sender program. We write it as $\text{putS}_x z$, or even more abstractly as $\text{putS}$, if $x$ and $z$ are clear from the context. It represents the $CH$-expression case $x$ of $(\text{Chan } c a_1 \ldots a_n \rightarrow \text{putMVar } c z \Rightarrow e)$ where $e$ is the remaining program of the sender.

**takeS:** The receiver takes the message from the contents-MVar of channel $x$ and replaces the channel name $y$ by the received name in the subsequent program (which does not belong the current input-operation). This operation occurs exactly once in the receive program. We write this as $\text{takeS}_x y$, or even more abstractly as $\text{takeS}$, if $x$ and $y$ are clear from the context.
It represents the $CH$-expression
\[
\text{case } x \text{ of } (\text{Chan } a_1 \ldots a_n \rightarrow \text{takeMVar } c \gg= \lambda y.e)\
\]
where $e$ is the remaining program of the receiver.

In do-notation, we also write $\text{do } \{ y \leftarrow \text{takeS}_x ; e \}$ to abbreviate the above $CH$-expression.

$\text{putC}$ and $\text{takeC}$: The sender and the receiver may synchronize on a check-MVar $\text{check}_1, \ldots, \text{check}_n$ by either putting () into it or by emptying the MVar. These operations are written as $\text{putC}_x^i$ and $\text{takeC}_x^i$, or even more abstractly as $\text{putC}_x$, $\text{takeC}_x$ if the name $x$ is clear from the context. We write $\text{putC}$ and $\text{takeC}$ if there is only one check-MVar.

Let $e$ be the remaining program of the sender or receiver. Then $\text{putC}_x^i$ represents the $CH$-program
\[
\text{case } x \text{ of } (\text{Chan } a_1 \ldots a_n \rightarrow \text{putMVar } a_i () \gg e)\
\]
and $\text{takeC}_x^i$ represents the $CH$-program
\[
\text{case } x \text{ of } (\text{Chan } a_1 \ldots a_n \rightarrow \text{takeMVar } a_i \gg e)\
\]

We restrict our search for translations to the case that the sender and the receiver programs are sequences of the above operations, and that they are independent of the channel name $x$. With this restriction we can abstractly write the translation of the sender and the receiver program as a pair of sequences, where only $\text{putS}$, $\text{takeS}$, $\text{putC}_x$ and $\text{takeC}_x$ operations are used. We make some more restrictions in the following definition:

**Definition 5.1.** Let $n > 0$ be a number of check-MVars. A standard global synchronized-to-buffer translation (or gstb-translation) is represented as a pair $(T_{\text{send}}, T_{\text{receive}})$ of a send-sequence $T_{\text{send}}$ and of a receive-sequence $T_{\text{receive}}$ which both consist of $\text{putS}$, $\text{takeS}$, $\text{putC}_x$ and $\text{takeC}_x$ operations, where

- The send-sequence contains $\text{putS}$ (exactly once), and the receive-sequence contains $\text{takeS}$ (exactly once).
- For every $\text{putC}_x^i$-action in $(T_{\text{send}}, T_{\text{receive}})$, there is also a $\text{takeC}_x^i$-action in $(T_{\text{send}}, T_{\text{receive}})$.
- We can assume that in the send-sequence the indices $i$ are ascending. I.e. if $\text{putC}_x^i$ or $\text{takeC}_x^i$ is before $\text{putC}_x^j$ or $\text{takeC}_x^j$, then $i < j$.

In the following we often say translation instead of gstb-translation, if this is clear from the context.

**Definition 5.2.** Let $T = (T_{\text{send}}, T_{\text{receive}})$ be a gstb-translation. We write $T_{\text{send}}^{x,y}$ for the program $T_{\text{send}}$ instantiated for the output-prefix $Tx$, i.e. the $\text{putS}$-operation is $\text{putS}_x y$, and all other operations are indexed with name $x$, i.e $\text{takeC}_x^i$ becomes $\text{takeC}_x^i$ and $\text{putC}_x^i$ becomes $\text{putC}_x^i$. We write $T_{\text{receive}}^{x,y}$ for the program $T_{\text{receive}}$ instantiated for the input-prefix $Ty$, i.e. the $\text{takeS}$-operation is $\text{takeS}_x y$, and all
other operations are indexed with name $x$, i.e. $\text{takeC}^i$ becomes $\text{takeC}^i_x$ and $\text{putC}^i$ becomes $\text{putC}^i_x$.

The induced translations $\phi_{0,T}$ and $\phi_T$ of $(T_{\text{send}}, T_{\text{receive}})$ are defined as follows, where $n$ is the number of check-MVars used by the translation:

\[
\begin{align*}
\phi_{0,T}(P) &= \text{main} \leftarrow \text{return} ()
\end{align*}
\]

\[
\begin{align*}
\phi_T(x)(P) &= \text{do } \{ \text{forkIO } \phi_T(P); \text{putMVar } \text{stop } () \}
\end{align*}
\]

Note that the induced translations are defined similar to the translation $\tau_0$ and $\tau$, where the differences are the representations of the channel, and thus the translation of $\nu x, x(y)$, and $\pi y$ is changed, while the other cases remain the same. Especially, it holds that $\phi_{0,T}(P) = C^\text{out}_{T_{\text{send}}}[\phi_T]$. Using the same arguments as in the proof of Theorem 4.10 we can show the following proposition.

**Proposition 5.3.** If $\phi_T$ is closed convergence equivalent, then $\phi_T$ is adequate and on closed processes it is fully-abstract.

We also speak of an execution of a translation $(T_{\text{send}}, T_{\text{receive}})$ for name $x$. Here we mean the simulation of the abstract program, i.e. a program that starts with empty MVars $x, x_1, \ldots, x_n$, and is an interleaved sequence of actions from the send and receive-sequence $T_{\text{send}}$ and $T_{\text{receive}}$, respectively.

### 5.1 Classifying Translations

To speak about the translations we make some more classifications:

**Number $n$ of check-MVars** We classify the translations by the number of check-MVars that are present in the Channel-type

**Multiple-Uses** We say that a translation allows multiple uses, if the same check-MVar is used more than once, i.e. the sender and/or receiver may contain $\text{takeC}^i$ and $\text{putC}^i$ more than once for the same $i$.

**Interprocess check restriction** We say that a translation has the interprocess check restriction, if for every $i$: $\text{takeC}^i$ and $\text{putC}^i$ do not occur both in $T_{\text{send}}$, and also not in $T_{\text{receive}}$.

We define some properties of gstb-translations:

**Definition 5.4.** A translation $(T_{\text{send}}, T_{\text{receive}})$ according to Definition 5.1 is
– executable, if there is a deadlock free interleaving execution of the parallel combination of $T_{send}$ and $T_{receive}$.
– communicating, if $T_{send}$ contains at least one takeC-acton.
– overlap-free, if for a fixed name $x$, starting with empty MVars, every interleaved (concurrent) execution of $T_{send}$ and $T_{receive}$ cannot be disturbed by starting another $T_{send}$ and $T_{receive}$. More formally, let $((s_1;...;s_i);(r_1;...;r_j))$ and $((s'_1;...;s'_i);(r'_1;...;r'_j))$ be two copies of $(T_{send}, T_{receive})$ for a fixed name $x$. We call a command $a_k$ from one of the four sequences, an $a$-action for $a \in \{s,s',r,r'\}$. The translation $T$ is overlap-free if every execution of the four sequences has the property that it can be split into a prefix and a suffix (called parts in the following) such that one of the following properties holds

- one part contains only $s$- and $r$-actions, and the other part contains only $s'$- and $r'$-actions, or
- one part contains only $s$- and $r'$-actions and the other part contains only $s'$- and $r$-actions.

5.2 Simulating Translations to Refute their Correctness

We implemented a tool to enumerate translations and then to test whether every translation preserves and reflects may- and should-convergence. The above mentioned parallel execution of $T_{send}$ and $T_{receive}$ is not sufficient to refute most of the translations, since it corresponds to the evaluation of the $\pi$-program $\nu x.(x(y) | x z)$ (which is must-divergent, since no Stop occurs). Thus our approach is to apply the translation to a subset of $\pi$-process for which we can decide may- and should-convergence (before and after the translation) in an automated way. Thus we consider only $\pi$-programs of the form $(\nu x_1, \ldots, x_n.P)$ where $P$ contains only 0, Stop, parallel composition $|$, and input- and output-prefixes. Thus, the programs are replication free and the $\nu$-binders are on the top, and hence terminate. In the following we omit the $\nu$-binders, but always mean them to be present.

Our simulation tool\(^6\) can execute all possible evaluations of such $\pi$-processes and – since all evaluation paths are of finite length, the tool can check for may- and should-convergence of the $\pi$-program. For the translated program, we do not generate a full CH-program: We generate a sequence of sequences of takeS, putS, takeC, putC and Stop-operations by applying the translation to all action prefixes in $\pi$-program and by translating Stop into Stop, 0 into an empty sequence. We get a sequence of sequences, since we have several threads and each thread is represented by such a sequence. For executing the translated program we simulate the global store (of MVars) and execute all possible inter-leavings where we check for may- and should-convergence by looking whether the Stop eventually occurs at the beginning of the sequence. This quite obviously simulates the behavior of the real CH-program but in a controllable manner.

Now having an encoding of the sender- and the receiver program, we use a $\pi$-calculus process $P$ and

\(^6\) Available via [https://gitlab.com/davidsabel/refute-pi](https://gitlab.com/davidsabel/refute-pi)
Embedding the Pi-Calculus into CH

1. Translate it with the encodings in the sequence of sequences consisting of 
   \[ \text{takeS}_x, \text{putS}_x, \text{takeC}_i, \text{putC}_i z \] and Stop-operations.
2. Simulate the execution on all interleavings.
3. Test may- and should convergence of the original \( \pi \)-program \( P \) as well as the encoded program (w.r.t. the simulation).
4. Compare the convergence before and after the translation. If there is a difference in the convergence behavior, then \( P \) is a counter-example for the correctness of the encodings.

5.3 Translations Without Check-MVars

The (trivial) case that there is no check-MVar leads to one possible translation 
\( [\text{putS}_z, \text{takeS}_x] \) which means that \( \tau z \) is translated into \( \text{putS}_x y \) and \( x(y) \) is translated into \( \text{takeS}_x y \). This translation is not correct, since for instance the \( \pi \)-process \( \tau z.x(y).\text{Stop} \) is neither may- nor should-convergent, but the translation \( \text{putS}_x z; \text{takeS}_x y; \text{Stop} \) is may- and should-convergent, since the put- and the take-operation can be done sequentially.

5.4 Translations With Interprocess Check Restriction

We first consider translations with the interprocess check restriction. In this case the number of different translations is \( n! * 2^n * (n + 1)^2 \) (where \( n \) is the number of check-MVars), which for \( n = 1 \) results in 8, for \( n = 2 \) results in 72, and for \( n = 3 \) in 768 translations.

For a single check-MVar all 8 translations are rejected by our simulation, Table 1 shows the translations and the obtained counter examples.

<table>
<thead>
<tr>
<th>Translation (Sender,Receiver)</th>
<th>Counter-example (( \pi )-process)</th>
<th>may-conv before</th>
<th>should-conv before</th>
<th>may-conv after</th>
<th>should-conv after</th>
</tr>
</thead>
<tbody>
<tr>
<td>([\text{putC},\text{putS}], [\text{takeC},\text{takeS}])</td>
<td>( \tau y.x(y).\text{Stop} )</td>
<td>N N Y Y</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>([\text{putC},\text{putS}], [\text{takeC},\text{takeS}])</td>
<td>( \tau y.x(y).\text{Stop} )</td>
<td>N N Y Y</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>([\text{putS},\text{putC}], [\text{takeC},\text{takeS}])</td>
<td>( \tau y.x(y).\text{Stop} )</td>
<td>N N Y Y</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>([\text{putC},\text{putS}], [\text{tagC},\text{tagS}])</td>
<td>( \tau y.x(z).\text{Stop} \times (w) )</td>
<td>N N Y N</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>([\text{tagC},\text{tagS}], [\text{tagC},\text{tagS}])</td>
<td>( \tau y.x(z).\text{Stop} )</td>
<td>Y Y N N</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>([\text{tagC},\text{tagS}], [\text{tagS},\text{tagC}])</td>
<td>( \tau y.z.x.y(x).\text{Stop} \times (w) )</td>
<td>N N Y N</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>([\text{tagC},\text{tagS}], [\text{tagS},\text{tagC}])</td>
<td>( \tau z.x.\text{Stop} \times w.\text{Stop} \times (y)(u) )</td>
<td>Y Y Y N</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Table 1. Translations using one check-MVar and with the interprocess check restriction.
Remark 5.5. We exhibit an example, why the translation
\[
(T_{\text{send}}, T_{\text{receive}}) = (\text{putS, takeC}, \text{[takeS, putC]})
\]
is not correct. Consider the following $N_{\text{Stop}}$-process
\[
P = \nu x, z, w. a. (\text{zx, z, Stop} | \text{w, w, Stop} | x(y). y(w). 0)
\]
which is should-convergent, since the two possible reduction sequences starting
with $P$ end in successful processes.

Consider the translated $CH$-program where we use the commands abbreviating
the concrete $CH$-programs, and where we assume that the program code
for creating the MVars and the bindings for the Channel-objects are already
executed:

\[
\nu. . . x = \text{Chan } c_x \text{ chk}_x | z = \text{Chan } c_x \text{ chk}_x | w = \text{Chan } c_w \text{ chk}_w | . . .
\]
\[
| c_x m | \text{chk}_x m - | c_2 m | \text{chk}_x m - | c_w m | \text{chk}_w m - | . . .
\]
\[
| \text{do} \{\text{putS}_x z; \text{takeC}_x; \text{putS}_z a; \text{takeC}_z; . . .\}\}
\]
\[
| \text{do} \{\text{putS}_x w; \text{takeC}_x; \text{putS}_w a; \text{takeC}_w; . . .\}\}
\]
\[
| \text{do} \{y \leftarrow \text{takeS}_x; \text{putC}_x; . . .\}\}
\]

This reduces after executing $\text{putS}_x z$ and $y \leftarrow \text{takeS}_x$:

\[
\nu. . . x = \text{Chan } c_x \text{ chk}_x | z = \text{Chan } c_x \text{ chk}_x | w = \text{Chan } c_w \text{ chk}_w | . . .
\]
\[
| c_x m | \text{chk}_x m - | c_2 m | \text{chk}_x m - | c_w m | \text{chk}_w m - | . . .
\]
\[
| \text{do} \{\text{takeC}_x; \text{putS}_x a; \text{takeC}_z; . . .\}\}
\]
\[
| \text{do} \{\text{putS}_x w; \text{takeC}_x; \text{putS}_w a; \text{takeC}_w; . . .\}\}
\]
\[
| \text{do} \{w \leftarrow \text{takeS}_z; . . .\}\}
\]

The second and third thread make steps:

\[
\nu. . . x = \text{Chan } c_x \text{ chk}_x | z = \text{Chan } c_x \text{ chk}_x | w = \text{Chan } c_w \text{ chk}_w | . . .
\]
\[
| c_x m | \text{chk}_x m - | c_2 m | \text{chk}_x m - | c_w m | \text{chk}_w m - | . . .
\]
\[
| \text{do} \{\text{takeC}_x; \text{putS}_z a; \text{takeC}_z; . . .\}\}
\]
\[
| \text{do} \{\text{takeC}_x; \text{putS}_w a; \text{takeC}_w; . . .\}\}
\]
\[
| \text{do} \{w \leftarrow \text{takeS}_z; . . .\}\}
\]

The second thread can make an unexpected step:

\[
\nu. . . x = \text{Chan } c_x \text{ chk}_x | z = \text{Chan } c_x \text{ chk}_x | w = \text{Chan } c_w \text{ chk}_w | . . .
\]
\[
| c_x m w | \text{chk}_x m - | c_2 m | \text{chk}_x m - | c_w m | \text{chk}_w m - | . . .
\]
\[
| \text{do} \{\text{takeC}_x; \text{putS}_z a; \text{takeC}_z; . . .\}\}
\]
\[
| \text{do} \{\text{putS}_w a; \text{takeC}_w; . . .\}\}
\]
\[
| \text{do} \{w \leftarrow \text{takeS}_z; . . .\}\}
\]

Now the only possible step is

\[
\nu. . . x = \text{Chan } c_x \text{ chk}_x | z = \text{Chan } c_x \text{ chk}_x | w = \text{Chan } c_w \text{ chk}_w | . . .
\]
\[
| c_x m w | \text{chk}_x m - | c_2 m | \text{chk}_x m - | c_w m a | \text{chk}_w m - | . . .
\]
\[
| \text{do} \{\text{takeC}_x; \text{putS}_z a; \text{takeC}_z; . . .\}\}
\]
\[
| \text{do} \{\text{takeC}_w; . . .\}\}
\]
\[
| \text{do} \{w \leftarrow \text{takeS}_z; . . .\}\}
\]

and now the process is stuck.
If we use 2 check-MVars, then there are 72 translations. Again our tool is able to refute all of them. Compared to Table 1, there are two further \( \pi \)-processes used, which act as counterexample.

However, one can also use the processes \( \overline{x}y.\text{Stop} \mid x(y) \) and \( \overline{xy}.\text{Stop} \mid x(y) \mid \overline{x}z \mid y(u).\text{Stop} \) to refute all 72 translations.

For 3 MVars, there are 768 translations, where 762 are rejected (where the counter examples are the same as for 2-check-MVars) and 6 potentially correct translations remain. The six translations are shown in Table 2.

**Table 2.** Non-rejected translations for 3 check-MVars and interprocess check restriction

<table>
<thead>
<tr>
<th>Translations Sender</th>
<th>Receiver</th>
</tr>
</thead>
<tbody>
<tr>
<td>( T_1 ) ( [\text{putS,putC,\text{takeC},\text{putC}]^2] )</td>
<td>( [\text{takeC,putC,\text{takeC},\text{takeS}]} )</td>
</tr>
<tr>
<td>( T_2 ) ( [\text{takeC,putS,\text{takeC},\text{putC}]} )</td>
<td>( [\text{putC,putC,\text{takeS,putC}]} )</td>
</tr>
<tr>
<td>( T_3 ) ( [\text{putC,putS,\text{takeC},\text{putC}]} )</td>
<td>( [\text{takeS,putC,\text{takeC},\text{takeC}]} )</td>
</tr>
<tr>
<td>( T_4 ) ( [\text{putC,putC,\text{takeC},\text{putS}]} )</td>
<td>( [\text{takeC,putC,\text{takeC},\text{takeC}]} )</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Translations with potential overlaps</th>
</tr>
</thead>
<tbody>
<tr>
<td>( T_5 ) ( [\text{takeC,putS,\text{takeC},\text{putC}]} )</td>
</tr>
<tr>
<td>( T_6 ) ( [\text{putC,\text{takeC},\text{putS,\text{takeC}]} )</td>
</tr>
</tbody>
</table>

**Theorem 5.6.** There is no valid gsth-translation with the interprocess check restriction for less than three check-MVars.

**Proof.** (Sketch) Our simulator refuted all translations for less than three check-MVars: All 72 possibilities of translations using 2 check-MVars could be refuted, where two processes suffice to refute all translation variants: \( \overline{x}y.\text{Stop} \mid x(y) \) and \( \overline{xy}.\text{Stop} \mid x(y) \mid \overline{x}z \mid y(u).\text{Stop} \), where the first process is should-convergent and the latter is neither may- nor should-convergent.

A reason for the failure of translations with less than three check-MVars may be the following result:

**Theorem 5.7.** There is no executable, communicating, and overlap-free gsth-translation with the interprocess check restriction for \( n < 3 \).

**Proof.** For \( n = 1 \) we check the last four translations in Table 1. The first four are non-communicating. For \( ([\text{putS,\text{takeC},[\text{takeS,putC}]}]) \): after executing \( \text{putS,\text{takeS}} \) we can execute \( \text{putS} \) again. For \( ([\text{putS,\text{takeC},[\text{putC,\text{takeS}]}}) \): after executing \( \text{putC,\text{putS},\text{takeC}} \) we can execute \( \text{putC} \) again. For \( ([\text{takeC,\text{putS},[\text{takeS,\text{putC}]}]) \): A deadlock occurs immediately. Thus the translation is not executable. For \( ([\text{takeC,\text{putS},[\text{putC,\text{takeS}}]) \): After executing \( \text{putC,\text{takeC}} \), we can execute \( \text{putC} \) again. For \( n = 2 \), we have checked all cases using our simulator, and no executable, communicating, and overlap-free translation for \( n = 2 \) is found. The
numbers are: 18 translations are ruled out, since these are non-communicating, 21 lead to a deadlock, and 33 may lead to an overlap. This check could also be done by hand by scanning all 72 translations.

**Proposition 5.8.** The first four translations in Table 2 are executable, communicating, and overlap-free, whereas the next two translations are executable, communicating, but are overlapping.

*Proof.* The executability and overlap-freeness can be easily checked by simulating the possible executions. In any case, only if all 8 actions are finished, the next send or receive can start.

For the next two translations the overlap shows up after executing $\text{putC}^1_1$, $\text{takeC}^1_1$, since we can again execute $\text{putC}^1_1$. □

In Appendix B.3 we sketch the arguments that the induced translation from the first gstb-translation from Table 2, i.e. $\phi_{T_1} = (T_{\text{send}}, T_{\text{receive}}) = ([\text{putS}, \text{putC}^1_1, \text{takeC}^2_1, \text{putC}^3_1], [\text{takeC}^1_1, \text{putC}^2_2, \text{takeC}^3_3, \text{takeS}])$, is indeed correct and leaves may- and should convergence invariant. The main help in reasoning is that there is no unintended interleaving of send and receive sequences according to Proposition 5.8. Application of Proposition 5.3 shows the following theorem.

**Theorem 5.9.** Translation $\phi_{T_1}$ is adequate and on closed processes it is fully-abstract.

For 4 MVars there are 9600 translations and our simulation refutes 9266 and thus there remain 334 candidates for correct translations.

### 5.5 Dropping the Interprocess Check Restriction

If we drop the interprocess check restriction, but only allow one check-MVar without reuse, then we get 20 candidates for translations, which are all refuted by our simulation.

Allowing reuse of the single check-MVar seems not to help to construct a correct translation: We simulated this for up to 6 uses (which leads to 420420 candidates for a correct translation) – our simulation refutes all of them.

We conjecture that there is no correct translation for one check-MVar where re-uses are permitted, and where the interprocess check restriction is dropped.

For two MVars and one use but without the interprocess check-MVar there are 420 translations. Our simulation refutes all but two translations, which are shown in Table 3. In the translation $T_7$ the second check-MVar is used as a mutex for the senders, ensuring that only one sender can operate at a time, while the translation $T_8$ does the same on the receiver side.

**Proposition 5.10.** The translations $T_7$, $T_8$ in Table 3 are executable, communicating, and overlap-free.
Table 3. Non-refuted translations for two check-MVars, no interprocess check restriction

<table>
<thead>
<tr>
<th>Translation</th>
<th>Sender</th>
<th>Receiver</th>
</tr>
</thead>
<tbody>
<tr>
<td>$T_7$</td>
<td>[putC, putS, takeC, takeC]</td>
<td>[takeS, putC]</td>
</tr>
<tr>
<td>$T_8$</td>
<td>[takeC, putS]</td>
<td>[putC, putC, takeS, takeC]</td>
</tr>
</tbody>
</table>

Proof. It is easy to verify that the translations are executable and communicating. For the first translation, assume that $\text{putC}_1, \text{putS}$ and $\text{takeS}$ are performed in this order (no other order is possible). Then an additional sender cannot execute its first command before the original sender performs $\text{takeC}_1$ and this again is only possible after the receiver program is finished. An additional receiver can only be executed after a $\text{putS}$ is performed, which cannot be done by the current sender and receivers.

For the second translation, assume that $\text{putC}_2, \text{putC}_1$ and $\text{takeC}_1$ are performed in this order (no other order is possible). An additional receiver can only start after $\text{takeC}_2$ was executed by the original receiver, which can only occur after the original sender and receiver program are fully evaluated. An additional sender can only start after $\text{putC}_1$ has been executed again, but the current sender and receiver cannot execute this command.

In the appendix we sketch in Theorem B.4 that the induced translation from the gstb-translation $T_7$, i.e. is (closed) convergence equivalent.

Applying Proposition 5.3 this shows

**Theorem 5.11.** Translation $\phi T_7$ is adequate and on closed processes it is fully-abstract.

We are convinced that the same holds for the second translation. We conclude the statistics of our search for translations without the interprocess restriction:

- For 3 MVars there are 10080 translations and 9992 are refuted, i.e. 88 translations are potentially correct. Among the 98 translations, there is the translation $([\text{putC}_1, \text{putS}, \text{takeC}, \text{takeC}], [\text{putC}_3, \text{takeS}, \text{putC}, \text{takeC}])$ which is quite intuitive: check-MVar 1 is used as a mutex for all senders on the same channel, check-MVar 3 is used as a mutex for all receivers, and check-MVar 2 is used to acknowledge that the message was received.
- For 4 MVars there are 277200 translations and 273210 are refuted, i.e. 3990 translations are potentially correct.

6 Discussion and Conclusion

We have investigated the problem of translating the $\pi$-calculus into the $CH$-calculus modeling Concurrent Haskell and proved that there is a correct translation $\tau_0$ from the $\pi$-calculus into $CH$, using private names for the storage of every translated communication construct. This translation is an embedding for
closed processes w.r.t. $\sim_c$, and preserves the may-convergence behavior as well as the should-convergence behavior. The translation $\tau_0$ meets the gold standard, since even for open processes, it is adequate w.r.t. the respective contextual equivalences.

For translations that use global names, we started an investigation on exhibiting (potentially) correct translations. The result is that in the design space of all (global) translations we identified several minimal translations that are potentially correct. We characterized all incorrect “small” translations, where we left the issue open whether there is a correct translation with only one check-MVar with unrestricted usage.

For two particular global translations, we have shown that they are convergence equivalent, i.e. they preserve and reflect the may-convergence behavior and the should-convergence behavior on closed processes. We also proved adequacy of the translations (on open processes) and full-abstraction if closed processes are translated. The exact form of the translations were found by our implemented tool to search for translations and to refute their correctness. The tool also showed that there is no correct gstb-translation with the interprocess check restriction for less than 3 check-MVars.

Further work may be to consider extended variants of the $\pi$-calculus. We are convinced that adding recursion and sums can easily be built into the translation, while it might be challenging to encode mixed sums or (name) matching operators. For name matching operators, our current translation would require to test usual bindings in $CH$ for equality which is not available in core-Haskell. Solutions may either use an adapted translation or an adapted core language that supports so-called observable sharing [6,8]. The translation of mixed-sums into $CH$ appears to require more complex translations, where the send and receive-parts are not linear lists of actions.

Another interesting research question for future work is whether the pi-calculus can be embedded in call-by-need lambda calculi with amb. Note that [4] show that the amb-operator is encodable in the $\pi$-calculus.

Acknowledgments

We thank an anonymous reviewer for advises to improve the translation. In particular for providing the counter-example in the last row of Table 1.
References


Embedding the Pi-Calculus into CH

\[ \sigma_0(P) = \nu stop. (\text{putMVar} \ stop () \ | \ stop m() \ | \ \sigma(P)) \]

\[ \sigma(\nu x.P) = \nu x, \text{chan}x. (\text{chan}m - \ | x = \text{Channel} \ \text{chan}x \ | \ \sigma(P)) \]

\[ \sigma(\pi y.P) = \pi \sigma(\text{chan}y.P) \]

\[ \sigma(\nu x.P) = \nu \tau(\nu x.y.P) \]

\[ \sigma(0) = \beta \text{return} () \]

\[ \sigma(\text{Stop}) = \beta \text{takeMVar} \ stop \]

\[ \sigma(P \ | \ Q) = \sigma(P) \ | \ \sigma(Q) \]

\[ \sigma(!P) = \nu f. (\beta \ f \ | f = \text{do} \{ \text{forkIO} \ \tau(P); f\}) \]

Fig. 10. Translations \( \sigma \) and \( \sigma_0 \)

A Convergence Properties of the Translation \( \tau_0 \)

A.1 A Top-Down Variant \( \sigma \) of \( \tau \)

We define a variant \( \sigma \) of the translation \( \tau \). It is intended to simplify the proofs. Instead of generating CH-programs that during their run generate concurrent processes and MVars (as \( \tau \) does), the translation \( \sigma \) generates CH-processes, which is closer to a direct implementation. The translation \( \sigma \) only modifies a top-part, and refers to \( \tau \) for the deeper translation parts that generate expressions. The main reason for introducing the translation \( \sigma \) is that it simplifies the proofs of correctness (of translations \( \tau \) and \( \tau_0 \)).

Definition A.1. The translation \( \sigma_0 \) is similar to \( \tau_0 \), and the translation \( \sigma \) also generates processes. Both translations are defined in Fig. 10. The translations of contexts are defined analogously where the context hole is translated into the context hole, i.e. \( \sigma([\,]) = [\,] \).

Definition A.2. The context \( C_\sigma \) for translation \( \sigma_0 \) is defined as

\[ C_\sigma = \nu f, \text{stop} (\beta \ \text{putMVar} \ stop (\ | \ \text{stop} m() \ | \ \beta \ [\,]) \]. \]

The following strong relationship between the translations \( \tau_0 \) and \( \sigma_0 \) (\( \tau \) and \( \sigma \), resp.) holds and is helpful in proofs for the translation \( \tau \):

Lemma A.3. For all \( P \in \Pi \text{Stop} \).

Proof. For the first part, it suffices to verify that \( \tau_0(P) \) is a process that reduces to \( \sigma_0(P) \) as follows:

\[ \tau_0(P) = \beta \text{do} \{ \text{stop} \leftarrow \text{newMVar} (); \text{forkIO} \ \tau(P); \text{putMVar} \ stop () \} \]

\[ \text{sr, mvar, } \text{sr, beta} \rightarrow \text{sr, beta} \rightarrow \nu \text{stop. \ stop m()} \ | \ \beta \text{do} \{ \text{forkIO} \ \tau(P); \text{putMVar} \ stop () \} \]

\[ \nu \text{stop. } \beta \text{do} \{ \text{forkIO} \ \tau(P); \text{putMVar} \ stop () \} \]

\[ \beta \text{do} \{ \text{forkIO} \ \tau(P); \text{putMVar} \ stop () \} \]

The \( \beta \text{do} \{ \text{forkIO} \ \tau(P); \text{putMVar} \ stop () \} \) sequence is derived from the second part of the lemma, and by plugging-in the reduction sequence in the larger context. It remains to show
the second part. We show this by induction on the size of \( P \) and by checking all the cases. If \( P \) starts with an input or an output prefix or is \( \text{Stop} \), then \( \Leftarrow \tau(P) = \sigma(P) \), and thus the claim holds. If \( P \) is the process \( 0 \), then \( \Leftarrow \tau(P) = \Leftarrow \text{return}() = \sigma(P) \). If \( P \) is a parallel composition \( P_1 \parallel P_2 \), then

\[
\Leftarrow \tau(P_1 \parallel P_2) = \Leftarrow \text{do} \{ \text{forkIO} \ \tau(P_2); \tau(P_1) \}
\]

The final \( \text{sr}\)-sequence obtained as follows: by the induction hypothesis we have \( \Leftarrow \tau(P_i) \rightarrow^{\text{sr}} \sigma(P_i) \) for \( i = 1,2 \) by combing and processing them sequentially we get \( \Leftarrow \tau(P_2) \parallel \Leftarrow \tau(P_1) \rightarrow^{\text{sr}} \sigma(P_2) \parallel \Leftarrow \tau(P_1) \rightarrow^{\text{sr}} \sigma(P_2) \parallel \sigma(P_1) \).

If the process starts with \( \nu x \), then

\[
\Leftarrow \tau(\nu x. P) = \Leftarrow \text{do} \{ \text{chanx} \leftarrow \text{newMVar} \bot; \text{takeMVar} \ \text{chanx};
\hspace{1cm} \text{letrec} \ x = \text{Channel} \ chanx \ \text{in} \ \tau(P) \}
\]

\[
\rightarrow^{\text{sr,tmvar, sr,beta}} \nu x. \text{chanx}. (\text{chanx} \ m -)
\hspace{1cm} \Leftarrow \text{do} \{ \text{takeMVar} \ \text{chanx};
\hspace{2cm} \text{letrec} \ x = \text{Channel} \ chanx \ \text{in} \ \tau(P) \}
\]

\[
\rightarrow^{\text{sr,mkbinds}} \nu x. \text{chanx}. (\text{chanx} \ m -) \Leftarrow \text{letrec} \ x = \text{Channel} \ chanx \ \text{in} \ \tau(P)
\]

\[
\rightarrow^{\text{sr, *}} \nu x. \text{chanx}. (\text{chanx} \ m -) \Leftarrow \text{letrec} \ x = \text{Channel} \ chanx \ \text{in} \ \tau(P)
\]

Note that the (tmvar)-reduction is deterministic (i.e. a (dtmvar)-transformation), since there is no alternative, and since the names of the visibility of the MVars and the potential accesses leave only one possibility. The final standard reduction sequence is obtained by first applying the induction hypothesis for \( P \) to derive \( \Leftarrow \tau(P) \rightarrow^{\text{sr, *}} \sigma(P) \) and then observing that the same reduction sequence can be performed if there are more parallel components.

If the process is a replication, then

\[
\Leftarrow \tau(!P) = \Leftarrow \text{letrec} \ f = \text{do} \{ \text{forkIO} \ \tau(P); f \} \ \text{in} \ f
\]

\[
\rightarrow^{\text{sr,mkbinds}} \nu f. \Leftarrow f \ \text{do} \{ \text{forkIO} \ \tau(P); f \} = \sigma(!P)
\]

This finishes the induction proof. \( \square \)

Lemma \( A.3 \) and correctness of (fork), (nmvar), (dtmvar), (beta), (cpce), and (mkbinds) (see Proposition \( 3.6 \)) imply:

**Proposition A.4.** For all \( P \in \Pi_{\text{Stop}} \): 1. \( \sigma(P) \sim_{c} \tau(P) \) and 2. \( \sigma_{0}(P) \sim_{c} \tau_{0}(P) \). Hence also \( \sigma(P) \sim_{c, \tau_{0}} \tau(P) \) and \( \sigma_{0}(P) \sim_{c, \tau_{0}} \tau_{0}(P) \).

### A.2 Preservation of May-Convergence

The goal of this section is to show that may-convergence of a closed \( \Pi_{\text{Stop}} \)-process \( P \) implies may-convergence of the translated process \( \tau_{0}(P) \). The main part is to show that for a single step \( P \xrightarrow{\text{dia}} P' \) or \( P \xrightarrow{\text{dsc}} P' \) for closed \( \Pi_{\text{Stop}} \)-processes \( P, P' \), there are corresponding reduction steps of \( \sigma_{0}(P) \) in the \( \text{CH} \)-calculus.
Lemma A.5. Let $P \in \Pi_{\text{stop}}$ be a closed process with $P \xrightarrow{\text{dia}_{\text{stop}}} P'$. Then there is a sequence $\sigma_0(P) \xrightarrow{sr, *} \sigma_0(P')$ in the CH-calculus, where $G$ is a closed process that can be seen as garbage, i.e. $\sigma_0(P') \xrightarrow{\text{gc}} \sigma_0(P^n)$.

Proof. Let $P = \mathbb{D}[x(y).P] | x.z.P]$ and $P' = \mathbb{D}[P_1[z/y] | P_3]$. Since $P$ is closed, there is a binder $\nu x$ in $\mathbb{D}$, i.e. we can assume that $\mathbb{D} = \mathbb{D}_1[\nu x.\mathbb{D}_2[\cdot]]$ for some $\mathbb{D}_x$-contexts $\mathbb{D}_1, \mathbb{D}_2$. Since $\mathbb{D}_x$-contexts have no input- or output-prefix on the hole-path, this shows:

$$
\sigma_0(P) = C_{\text{out}}[D_1[\nu x, \text{chanx}.\text{chanx.m} - | x = \text{Channel}. \text{chanx} \\
D_2[\left\{ \text{checkx} \leftarrow \text{newMVar} (); \\
\text{putMVar} (\text{unchan} x) (z, \text{checkx}); \\
\text{putMVar} \text{checkx} (); \nu \tau (P_x) \right\}] \\
\left\{ \text{checkx} \leftarrow \text{takeMVar} (\text{unchan} x); \\
\text{takeMVar} \text{checkx}; \nu \tau (P_x) \right\}]]
$$

where $D_1, D_2$ are the $\sigma$-translations of $\mathbb{D}_1, \mathbb{D}_2$. Inspecting $\sigma$ shows, that $D_1, D_2$ are $\mathbb{D}$-contexts. There is a sequence of $sr, \ast$-reductions (see Fig. 11) such that

$$
\sigma_0(P) \xrightarrow{sr, 15} G = C_{\text{out}}[D_1[\nu x, \text{chanx}.\text{chanx.m} - | x = \text{Channel}. \text{chanx} \\
D_2[\left\{ \nu \tau (P_x) \right\}] \\
\left\{ \text{checkx} \leftarrow \text{newMVar} () \right\} \\
\left\{ \text{checkx} \leftarrow \text{takeMVar} (\text{unchan} x); \\
\text{takeMVar} \text{checkx}; \nu \tau (P_x) \right\}]]
$$

where $G = (\nu \text{checkx}.\text{checkx.m()}$) is garbage. Since

$$
\sigma_0(P') = C_{\text{out}}[D_1[\nu x, \text{chanx}.\text{chanx.m} - | x = \text{Channel}. \text{chanx} \\
D_2[\sigma(P_1[z/y]) | \sigma(P_3)]]],
$$

Lemma A.3 implies $Q \xrightarrow{sr, \ast} \sigma_0(P') | \nu \text{checkx}.\text{checkx.m()} \xrightarrow{\text{gc}} \sigma_0(P')$.

Lemma A.6. Let $P, P' \in \Pi_{\text{stop}}$ be closed, such that $P \xrightarrow{\text{dia}_{\text{stop}}} P'$. Then $\sigma_0(P) \xrightarrow{sr, \ast} \sigma_0(P')$, if rule (replunfold) is used, and $\sigma_0(P) \equiv \sigma_0(P')$, otherwise. In particular, $\sigma_0(P) \sim_{\nu} \sigma_0(P')$, and hence $\sigma_0(P) \sim_{\nu, 0} \sigma_0(P')$.

Proof. For rules (assocl) and (assocr), we have

$$
\sigma_0(\mathbb{D}[(P_1 | P_2) | P_3]) = C[(\sigma(P_1) | \sigma(P_2)) | \sigma(P_3)] \\
= C[\sigma(P_1) | \sigma(P_2)]
$$

for some $\mathbb{C} \mathbb{D}$-context $C$ and thus, $\sigma_0(P) \equiv \sigma_0(P')$. For (nuup1), we have

$$
\sigma_0(\mathbb{D}[\nu z.\nu z.P_1 | P_2]) = C[\sigma(\nu \nu z.\nu z.P_1) | \sigma(P_2)]
$$

for some $\mathbb{C} \mathbb{D}$-context $C$. Thus, $\sigma_0(P) \equiv \sigma_0(P')$. For (nuup2), we have

$$
\sigma_0(\mathbb{D}[\nu z.\nu x.P]) = C[\sigma(\nu z.\nu x.P_1)]
$$

for some $\mathbb{C} \mathbb{D}$-context $C$. Thus, $\sigma_0(P) \equiv \sigma_0(P')$. For (nuup2), we have
\(\sigma_0(P) = C^\sigma_{\text{out}}[D_1[vx, chanz.chanz m \mid x = \text{Channel chanz}]
\mid D_2 \Leftarrow \text{do \{checkz } \leftarrow \text{newMVar }(); \text{putMVar } (\text{unchan } x) (z, \text{checkx}); \\
\text{putMVar } \text{checkx }(); \tau(P_s)\}]
\mid l \Leftarrow \text{do \{(y, checkx)} \leftarrow \text{takeMVar } (\text{unchan } x); \\
\text{takeMVar } (\text{checkx}); \tau(P_s)\}]]
\)

\(\xrightarrow{\text{sr.mvar, sr.beta}} \)

\(\sigma_0^\text{out}[D_1[vx, chanz, checkx.chanz m \mid x = \text{Channel chanz}]
\mid D_2 \Leftarrow \text{do \{putMVar } (\text{unchan } x) (z, \text{checkx}); \\
\text{putMVar } \text{checkx }(); \tau(P_s)\}]
\mid l \Leftarrow \text{do \{(y, checkx)} \leftarrow \text{takeMVar } (\text{unchan } x); \\
\text{takeMVar } (\text{checkx}); \tau(P_s)\}]]
\)

\(\xrightarrow{\text{sr.epce, sr.case}} \)

\(\sigma_0^\text{out}[D_1[vx, chanz, checkx.chanz m \mid x = \text{Channel chanz}]
\mid D_2 \Leftarrow \text{do \{putMVar } (\text{unchan } x) (z, \text{checkx}); \\
\text{putMVar } \text{checkx }(); \tau(P_s)\}]
\mid l \Leftarrow \text{do \{(y, checkx)} \leftarrow \text{takeMVar } (\text{unchan } x); \\
\text{takeMVar } (\text{checkx}); \tau(P_s)\}]]
\)

\(\xrightarrow{\text{sr.pmvar, sr.beta}} \)

\(\sigma_0^\text{out}[D_1[vx, chanz, checkx.chanz m \mid x = \text{Channel chanz}]
\mid D_2 \Leftarrow \text{do \{putMVar } (\text{unchan } x) (z, \text{checkx}); \\
\text{putMVar } \text{checkx }(); \tau(P_s)\}]
\mid l \Leftarrow \text{do \{(y, checkx)} \leftarrow \text{takeMVar } (\text{unchan } x); \\
\text{takeMVar } (\text{checkx}); \tau(P_s)\}]]
\)

\(\xrightarrow{\text{sr.tmvar, sr.beta}} \)

\(\sigma_0^\text{out}[D_1[vx, chanz, checkx.chanz m \mid x = \text{Channel chanz}]
\mid D_2 \Leftarrow \text{do \{putMVar } (\text{unchan } x) (z, \text{checkx}); \\
\text{putMVar } \text{checkx }(); \tau(P_s)\}]
\mid l \Leftarrow \text{do \{\tau(P_s)} \leftarrow \text{do \{\tau(P_s)} (z/y)\}]]
\)

\(\equiv \)

\(\sigma_0^\text{out}[D_1[vx, chanz, checkx.chanz m \mid x = \text{Channel chanz}]
\mid D_2 \Leftarrow \text{do \{\tau(P_s)} \leftarrow \text{do \{\tau(P_s)} (z/y)\}]]
\mid l \Leftarrow \text{do \{\nu \text{checkx}\text{.checkx } m (\})}
\)

\(= Q\)

Fig. 11. Reduction sequence \(\sigma_0(P) \xrightarrow{\text{sr.15}} Q\) which is used in Lemma A.5.
for some CH △-context C. Thus, we have \( \sigma_0(P) \equiv \sigma_0(P') \). For rule (commute), we have \( \sigma_0(P) \equiv \sigma_0(P') \), since

\[
\sigma_0(\langle P_1 \parallel P_2 \rangle) = C[(\sigma(P_1) \parallel \sigma(P_2))] \equiv C[(\sigma(P_2) \parallel \sigma(P_1))] = \sigma_0(\langle P_2 \parallel P_1 \rangle)
\]

for some △-context C. Thus the claim holds.

For the rule (replunfold), we have:

\[
\sigma_0(\langle f \parallel f = \text{do } \{\text{fork} \; \text{IO} \; \tau(P); f\} \rangle) \xrightarrow{\text{sre,epce}} C[\nu f.(\lambda f f = \text{do } \{\text{fork} \; \text{IO} \; \tau(P); f\} | f = \text{do } \{\text{fork} \; \text{IO} \; \tau(P); f\} ]
\]

where \( f \notin \text{FV}(P) \). This shows \( \sigma_0(\langle f \parallel f \rangle) \xrightarrow{\text{sre}^*} \sigma_0(\langle f \parallel f \rangle) \), using the previous items, and Lemma A.3 for the last \( \text{sre}^* \)-transformation. The equivalence \( \sigma_0(P) \sim_c \sigma_0(P') \) follows from correctness of the used reduction steps (see Proposition A.6).

**Lemma A.7.** Let \( P \) be a closed CH-process such that \( P \xrightarrow{\text{sre}^*} \cdot \sim_c \cdot Q \). Then the following implications hold: i) If \( Q \downarrow \), then \( P \downarrow \), and ii) If \( Q \uparrow \), then \( P \uparrow \).

**Proof.** We only show the first part. The proof for the second part can be obtained by replacing \( \downarrow \) by \( \uparrow \). The proof is by induction on the length of the relational sequence. Let \( P \xrightarrow{\text{sre}^*} \cdot \sim_c \cdot P_1 \xrightarrow{\text{sre}^*} \sim_c P_2 \sim_c Q \), where \( Q \downarrow \). From \( P_2 \sim_c Q \) we derive that also \( P_2 \downarrow \), and hence also \( P_1 \downarrow \), since \( P_1 \xrightarrow{\text{sre}^*} P_2 \).

**Lemma A.8.** If a closed process \( P \) of the calculus \( \Pi_{\text{Stop}} \) is successful, then \( \sigma_0(P) \xrightarrow{\text{sre}^*} Q \) where \( Q \) is successful.

**Proof.** Process \( P \) must contain \( \text{Stop} \) in a △-context. Thus the translation generates a thread in a △-context, that executes \( \text{takeMVar } \text{stop} \) and \( Q \xrightarrow{\text{sr,tmvar}, \text{sr,pmvar}} D(\text{return } () \rangle \). For the second part of the lemma, it suffices to observe that \( \sigma \) and \( \tau \) do not generate a \( \text{putMVar} \) for the MVar \( \text{stop} \) (except in the context \( C_\text{out}^\sigma \)) and thus, the MVar can always be emptied and the main-thread thereafter can perform the \( \text{putMVar}-\text{command} \) to become successful.

**Proposition A.9.** Implication of may-convergence: Let \( P \) be a closed \( \Pi_{\text{Stop}} \)-process. If \( P \downarrow \), then \( \tau(P) \downarrow \).

**Proof.** Lemmas A.5 to A.8 imply that \( \sigma_0(P) \xrightarrow{\text{sre}^*} Q \), where \( Q \) is successful, using induction on the length of a standard reduction of \( P \), hence \( \sigma(P) \downarrow_0 \). By Proposition A.4 this also shows \( \tau_0(P) \downarrow_0 \) which is the same as \( \tau(P) \downarrow_0 \).

Note that in the previous proof it is irrelevant whether we remove the garbage that stems from translated \( \rightarrow_{\text{dia}} \)-steps or not: In the former case we know that \( (gc) \) is a correct program transformation and thus does not change convergence, and in the latter case we carry the garbage with the whole reduction sequence.
A.3 The Translation Reflects May-Convergence

In this section we show that may-convergence of $\sigma_0(P)$ for a closed $\Pi_{\text{Stop}}$-process $P$ implies may-convergence of $P$. We show that, by rearranging and extending the standard reduction sequence to a successful process, a standard reduction sequence of $\sigma_0(P)$ is obtained that corresponds to a standard reduction sequence of $P$ in the $\pi$-calculus. There are three essentially different actions that are executed by the standard reduction sequence of $\sigma_0(P)$, where the single reduction steps may be distributed in the reduction sequence: interaction-reductions, replication of the $!P$-operator, and reducing $\tau$-images to $\sigma$-images.

The following properties are easily checked for a translated closed $\Pi_{\text{Stop}}$-process $P$:

$\tau_0(P)$ is closed;

$\tau_0(P)$ contains the variable $\text{stop}$ only in expressions of the form $\leftarrow \text{takeMVar stop}$, and $\tau_0(P)$ is well-formed and well-typed.

**Proposition A.10.** Let $P \in \Pi_{\text{Stop}}$ be closed, $n \in \mathbb{N}$ and $\sigma_0(P) \xrightarrow{\text{sr},n} Q_1$ such that $Q_1$ is successful. Then there is a standard reduction sequence $P \xrightarrow{\text{sr},*} P_1$ and another standard reduction sequence $\sigma_0(P) \xrightarrow{\text{sr},*} Q'_1$ such that $Q'_1$ and $P_1$ are successful and $Q'_1 = \sigma_0(P_1)$.

**Proof.** We show that the following diagram holds by induction on the number of all reduction steps in $\text{Red} = \sigma_0(P) \xrightarrow{\text{sr},n} Q_1$.

\[
\begin{array}{c}
P \\
\xrightarrow{\text{sr},*} \\
\sigma_0 \xrightarrow{(\text{sr}(\pi_\text{out}) \pi_\text{in})),*} \\
\sigma_0(P) \\
\xrightarrow{\text{sr},n} \\
\sigma_0(P_1)(\text{succ.}) \xleftarrow{\text{sr}(\pi_\text{out}) \pi_\text{in})),*} \\
\sigma_0(P) \\
\xrightarrow{\text{sr},n} \\
Q_1(\text{succ.}) \end{array}
\]

In this diagram and in the diagram in Fig. 12 a plain arrow means a given and a dashed arrow means an existing reduction.

The base case is that $\sigma_0(P)$ is of the form $C_{\text{out}}[\pi][\leftarrow \text{takeMVar stop}]$, and it standard-reduces in two steps to a successful process. In this case, the only possible reason is that $P$ contains $\text{Stop}$ in a $\text{PCtxt}_\pi$-context, and $\sigma_0$ maps it to this subprocess. Then $P$ is successful.

Now we show the induction step. A picture of the proof structure is shown in Fig. 12. By the induction hypothesis, we have $P_1$, but we have to calculate the upper square for all possibilities of actions (interaction reductions, replication,
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\( \tau \)-to-\( \sigma \)-reduction, where we rearrange the \( CH \)-standard reduction sequence, extend it with missing reduction steps, which do not change success, and construct a corresponding standard reduction sequence in the \( \pi \)-calculus.

Abstractly, a single induction step is intended to do the following: first we identify reduction steps that make a complete or a partial execution that performs one of the three actions. There may be two threads affected by interaction, and otherwise only one thread is affected. We have to identify one particular (distributed) reduction subsequence \( S \) that can be executed as a prefix; i.e. \( Red \) can be rearranged to \( S; Red' \) having the same total effect. There are two possibilities: Either there is such a full subsequence \( S \) of reduction steps, or we only identify a prefix of such a subsequence, and the corresponding threads do not have further reduction steps (of the action) until \( Q_1 \) is reached. In the first case, everything is fine, and a process \( P_1 \) can be determined. In the second case, the subsequence has to be removed, or extended by the missing reduction steps. Since the thread(s) is/are stopped, these additional reduction steps are independent of all other reduction steps. Hence these can be virtually shifted after \( Q_1 \) (perhaps turning into non-sr-reductions) and determine \( Q_1' \). We will add the extra reduction steps to the prefix of the reduction sequence, and then obtain a complete subsequence.

Since the intention of the proof is to construct a reduction sequence between images of translation \( \sigma \), this enforces that sometimes \( \tau \)-translated parts have to be sr-reduced to their \( \sigma \)-translation, and that a (gc)-step has to be inserted as a \( \sim_c \)-step.

Referring to Definition A.1 of translation \( \sigma \), there are several cases. Let us consider the case that an image of an ia-reduction is the first action. Analyzing \( Red \) shows that, ideally, if we only look for the (newmvar)-, (tmvar)-, and (pmvar)-reductions, the reduction sequence starts as follows

\[
\begin{align*}
(ia-0) & \quad \text{check} x \leftarrow \text{newMVar}(); \\
(ia-1) & \quad \text{putMVar} ((unchan x) (y, \text{check} x)) \text{ in thread a}; \\
(ia-2) & \quad \text{takeMVar} ((unchan x)) \text{ in thread b } (\neq a); \\
(ia-3) & \quad \text{takeMVar} \text{ check} x \text{ in thread b}; \\
(ia-4) & \quad \text{putMVar} \text{ check} x () \text{ in thread a}.
\end{align*}
\]

However, there may be deviations, like interleaving of reduction steps in parallel threads, or an incomplete subsequence. We analyse the possibilities in a reduction sequence, where we omit the non-interfering interleaved actions. Of course, we assume that the programs have the commands for \( (ia-0);(ia-1);(ia-4) \) as a sequence in one thread-program and the commands for \( (ia-2);(ia-3) \) as a sequence in the other thread-program.

1. (ia-0) is the only action for \( \text{check} x \); no further action; thread \( a \) is blocked.
2. (ia-0);(ia-1) is performed in thread \( a \); no other thread uses the MVar \( (unchan x) \); thread \( a \) is blocked.
3. (ia-0);(ia-1);(ia-2) is performed first in thread \( a \), and some other thread \( b \) won the race for \( (ia-2) \), and then no further progress; threads \( a,b \) are blocked.
   \text{takeMVar} \text{ check} x \text{ is not possible in another thread, since the name is unknown to other threads. However, the MVar} \text{ (unchan x) is already available for other threads.}
4. \((ia-0);(ia-1);(ia-2);(ia-3)\) is performed with \((ia-0);(ia-1)\) in thread \(a\), and then \((ia-2);(ia-3)\) in thread \(b\); thread \(a\) is blocked, but thread \(b\) can proceed. MVar \textit{checkx} is not garbage.

5. \((ia-0);(ia-1);(ia-2);(ia-3);(ia-4)\) is performed with \((ia-0);(ia-1);(ia-4)\) in thread \(a\), and \((ia-2);(ia-3)\) in thread \(b\). Thread \(a\) is released after the full sequence, thread \(b\) after \((ia-3)\). Moreover, after \((ia-4)\), the MVar \textit{checkx} can no longer be used and is garbage.

We show that modifying the reduction sequence \(\textit{Red}\), keeping the \(sr\)-property, results in another reduction sequence \(\textit{Red}'\) that starts with the ideal 5 reductions, followed by \(\textit{Red}''\) also ends in a successful process, such that \(\textit{Red}''\) contains less standard-reduction steps.

Now we focus the \(ia\)-related reduction steps in \(\textit{Red}\). We remove the following subsequence: In case there is a thread such that after an \((ia-0)\) or after an \((ia-0);(ia-1)\) step, then the thread is blocked; Then we remove these reduction steps from \(\textit{Red}\). We do this in all cases.

Now we focus the earliest occurrence in \(\textit{Red}\) of an \((ia-2)\)-related reduction step. If the \((ia)\)-subsequence corresponding to the first one is complete, then we move it to the start of the sequence. Of course the non-MVar related \(sr\)-steps in the corresponding threads \(a, b\) are also moved. In the following we do not mention these \(sr\)-reductions.

Now let us consider the incomplete ones:

- If exactly \((ia-0);(ia-1);(ia-2)\) are in \(\textit{Red}\), then thread \(a, b\) do not further contribute to the success, however, MVar \((\textit{unchan} \ x)\) may be used for the success (triggered by another thread). We add the reductions \((ia-3)\) and \((ia-4)\) in order to complete the \(ia\)-sequence, which makes the reduction sequence longer, but does not add \((ia-2)\)-reduction steps.

- If exactly \((ia-0);(ia-1);(ia-2);(ia-3)\) are in \(\textit{Red}\), then we add the missing \((ia-4)\) step without changing success and the rest of the reduction sequence.

In order to apply the induction hypothesis, we insert a \((gc)\)-step to remove the check-MVar that was only used for one interaction step in the translation. This \((gc)\) is correct, hence we can represent it by a \(\sim c\)-step.

Let \(\Leftarrow \tau(P_a)\) and \(\Leftarrow \tau(P_b)\) be the processes that are left by the four reduction steps that simulate the \((ia)\)-reduction. It remains to check whether \(\Leftarrow \tau(P_a)\) and \(\Leftarrow \tau(P_b)\) reduce to \(\sigma(P_a)\) and \(\sigma(P_b)\), respectively. Here we use the same construction as above: The reduction steps that are already in the sequence are shifted to the left. Since the threads are blocked if there are missing reductions, we can add the missing reduction steps to the reduction sequence, keeping the property that the resulting process of the whole sequence is successful. For details, we refer to Lemma A.3. We obtain the upper square of the diagram, where also the number of reductions is strictly smaller for the remaining reduction sequence.

We now consider the case that the reduction is an image of a \((\text{replunfold})\) step. Then the corresponding reduction is the \((\text{fork})\)-reduction. In fact, it is a sequence \(\xrightarrow{\text{op}} \ x \xrightarrow{\text{fork}} \ x \xrightarrow{\beta} \). We can, following the reduction pattern in the proof of Lemma A.6, move the reduction steps to the front, which are corresponding to
a single replication. If reduction steps are missing, we can add the missing steps without disturbing the final success. Also, we can add the necessary reductions that may be missing to turn the $\tau(P_r)$ into $\sigma(P_r)$. Again we can construct the square diagram, as requested, where also in this case, the process that represents the final success may have changed.

Finally, we can apply the induction hypothesis, since the combined measure is strictly reduced, and we thus obtain a standard reduction in $\Pi_{\text{Stop}}$ to a successful process.

Propositions $\text{A.9}$ and $\text{A.10}$ imply:

**Proposition 4.5.** Let $P \in \Pi_{\text{Stop}}$ be closed. Then $\tau_0$ is convergence-equivalent for $\downarrow$, i.e. $P\downarrow$ is equivalent to $\tau_0(P)\downarrow$. This also implies that $P\uparrow$ is equivalent to $\tau(P)\uparrow$.

### A.4 Equivalence of Should-Convergence of the Translation

We argue that the translation $\tau$ is invariant w.r.t. should-convergence, where we work with may-divergence and where Proposition 4.5 is very useful since it is the induction base.

**Proposition A.11 (Preservation of May-Divergence).** Let $P \in \Pi_{\text{Stop}}$ be closed, $n \in \mathbb{N}$ and $P \xrightarrow{sr,n} P_1$ such that $P_1\uparrow$. Then there is a standard reduction sequence $\sigma_0(P) \xrightarrow{sr,\ast} Q$ with $Q\uparrow$.

**Proof.** We use induction on $n$ to show that $P \xrightarrow{sr,n} P_1$ where $P_1\uparrow$ implies $\sigma_0(P)(\xrightarrow{sr,\ast} \cdot \sim_c)\ast Q'$ where $Q'$ is must-divergent. The base case $n = 0$ is covered by Proposition 4.5. For the induction step, Lemmas $\text{A.5}$ and $\text{A.6}$ can be applied for the first reduction step and then the induction hypothesis can be applied to show the claim. Finally, we apply item ii) of Lemma $\text{A.7}$ to derive the claim.

The last case is to show that reflection of may-divergence holds, i.e. $\sigma_0(P)\uparrow \implies P\uparrow$. This is almost similar to the arguments for $\sigma_0(P)\downarrow \implies P_1\downarrow$ where some more arguments are needed to show that the final process remains must-divergent after the rearrangements and additions to the reduction sequence.

**Proposition A.12.** Let $P \in \Pi_{\text{Stop}}$ be closed, $n \in \mathbb{N}$ and $\sigma_0(P) \xrightarrow{sr,n} Q_1$ such that $Q_1\uparrow$. Then there is a standard reduction sequence $P \xrightarrow{sr,\ast} P_1$ with $P_1\uparrow$, and another standard reduction sequence $\sigma_0(P) \xrightarrow{sr,\ast} Q'_1$ such that $Q_1'\uparrow$ and $Q_1' = \sigma_0(P_1')$.

**Proof.** The following diagram gives an orientation on the goals of the proof and the induction on $n$.

```
\begin{center}
\begin{tikzpicture}
  \node (P) at (0,0) {$P$};
  \node (P1) at (1,0) {$P_1$};
  \node (Q1) at (2,0) {$Q_1$};
  \node (Q) at (2,1) {$\sigma_0(P)$};

  \draw[->] (P) -- (P1) node[midway,above] (P1a) {$\xrightarrow{sr,\ast}$};
  \draw[->] (P1) -- (Q1) node[midway,above] (Q1a) {$\xrightarrow{sr,n}$};
  \draw[->] (P) -- (Q) node[midway,above] (Qa) {$\xrightarrow{sr,\ast}$};
  \draw[->] (Q) -- (Q1) node[midway,above] (Q1b) {$\xrightarrow{sr,n}$};

  \node (P0) at (0,-1) {$\tau_0(P)$};
  \node (Q0) at (2,-1) {$\tau_0(P_1)$};

  \draw[->] (P0) -- (P1) node[midway,above] (P1b) {$\xrightarrow{\sim_c,\ast}$};
  \draw[->] (P1) -- (Q1) node[midway,above] (Q1c) {$\xrightarrow{\sim_c,\ast}$};
  \draw[->] (P0) -- (Q0) node[midway,above] (Q0a) {$\xrightarrow{\sim_c,\ast}$};
  \draw[->] (Q0) -- (Q1) node[midway,above] (Q1d) {$\xrightarrow{\sim_c,\ast}$};
\end{tikzpicture}
\end{center}
```
Most arguments for a construction of the the reduction sequence $\sigma_0(P) \xrightarrow{sr \cup \sim_c,*} \sigma_0(P_1)$ are already in the proof of Proposition A.10. However, the treatment of the incomplete (ia)-reduction sequences is different, so we make it explicit.

The cases are:

1. In the case that only the (ia-0) is present, we can omit it, since it is correct.
2. If at least (ia-0);(ia-1);(ia-2) is there, then we add the missing reductions. The argument is: If $\sigma_0(P) \xrightarrow{sr,*} Q\uparrow$ and $Q \xrightarrow{sr \cup \sim_c,*} Q'$ then also $Q'\uparrow$, and we can perform the extra reductions (like (ia-3);(ia-4) from $Q$, and then shift them closer to the (ia-0);(ia-1);(ia-2)-sequence. This makes the ia-sequence complete, and does not change the induction measure.
3. The case that the incomplete sequence is (ia-0);(ia-1), and (ia-2) is not present, needs more arguments: There are two possibilities:
   (a) There is a reduction sequence starting from $Q$, which contains the needed (ia-2). Then we will shift first the (ia-2) such that $Q \xrightarrow{(ia-2)} Q'$, where obviously $Q'\uparrow$. Then we shift the (ia-2) further to the left, such that we have the sequence (ia-0);(ia-1);(ia-2), and we can then use the other cases. Here we have modify the induction measure: first the number of (ia-2) in the reduction sequence to $Q$, and as second component the total number of (ia)-reductions in the sequence $\sigma_0(P) \xrightarrow{sr,*} Q$. We have removed more than one (ia)-reduction in the induction, hence we can apply the induction hypothesis.
   (b) There is no reduction sequence starting from $Q$ with the needed (ia-2). In this case the argument uses that the current CH-program is a translation of a $\Pi_{\text{Stop}}$-process. We remove the (ia-1) from the reduction sequence. The reduction leads to $Q_1$, which is the result of the reduction sequence leading to $Q$, but without the (ia-1)-reduction. Now there may be another thread that can access the MVar (unchan $x$), again using (ia-0);(ia-1) in the other thread. However, since from $Q$ there is no (ia-2) possible for MVar (unchan $x$), the same must hold for $Q_1$. Hence, the removal permits only small local changes, but there is still no successful reduction from $Q_1$. Hence $Q_1\uparrow$. Of course, we can now also remove the (ia-0)-reduction.

Note that we also have to use the (gc)-reduction removing the check-MVar in case of a complete (ia)reduction in CH, which adds a $\sim_c$ to the combined reduction sequence.

Using these arguments the proof is otherwise completely analogous to the proof of Proposition A.10, and we are done.

**Proposition 4.6.** Let $P \in \Pi_{\text{Stop}}$ be closed. Then $\tau_0$ is convergence-equivalent for $\downarrow$, i.e. $P\downarrow$ is equivalent to $\tau_0(P)\downarrow$. 
B  Proofs and Details on Translations with Global Names

B.1 Failing Translations with Less than Three Check-MVars and the Interprocess Check Restriction: Examples

Before we prove that the first translation of Table 2 is invariant for may- and should-convergence, we show, how similar translations with less check-MVars fail. The reason is usually that the translated sequences for send and receive are not forced to be completely executed. For example, the translation of $\tau y :: x(y) . \tau y . \text{Stop}$ with one check-MVar where the sender program is $[\text{putS}, \text{takeC}]$; and the receiver program is $[\text{putC}, \text{takeS}]$ confuses the sequence for the program consisting of two threads, and where the $\pi$-process is not may-convergent, but the translated program may-converges. The program is written in the first line as two threads, and the sequence of execution in the second line:

$$\begin{array}{c|ccccc}
\text{send} & 1 & 2 & 3 & 4 & 5 & 6 \\
\text{receive} & [\text{putS}, \text{takeC}^1, \text{takeC}^2] & [\text{takeC}^1, \text{putC}^2, \text{takeS}] \\
\end{array}$$

The situation is similar for all other translations using one or two check-MVars, and where every check-MVar is written and read only once per translated interact. Either the communication is not synchronous, or the sequences of send and receive that do not belong to the intended interact can be interleaved.

For documentation purposes, we detail an example for a bad behavior for a translation using two check-MVars and which is similar to the first one, where this example is a result of using our simulator. The translation is

$$\begin{array}{c|ccccc}
\text{send} & 1 & 2 & 3 & 4 & 5 & 6 \\
\text{receive} & [\text{putS}_x z, \text{putC}_{x_z}^1, \text{takeC}_{x_z}^2, \text{putS}_a z, \text{putC}_{a_z}^1, \text{takeC}_{a_z}^2, \text{Stop}] \\
\end{array}$$

The process in $\pi$-calculus notation is $\tau z . \tau a . \text{Stop} :: \tau w . \tau a . \text{Stop} :: x(y) . y(u)$, which is should-convergent. We write the variable name of the value-MVar also at the check-MVar commands. Note that after a $\text{takeC}_x y$, the $y$ in the rest of the thread is changed accordingly.

The program consists of three threads, where the sequence of the problematic execution is:

$$\begin{array}{c|ccccc}
\text{send} & 1 & 2 & 3 & 4 & 5 & 6 \\
\text{receive} & 6 & 7 & 8 & 9 & 10 \\
\end{array}$$

The final program is deadlocked but not successful, hence this translation is not invariant for should-convergence.
\[ \psi_{0,T}(P) = \nu \text{stop}(\text{putMVar stop} \ | \ \text{stop m} | \ \psi_T(P)) \]

\[ \psi_T(\nu x . P) = \nu x, \text{contx}, \text{checkx}_1, \ldots, \text{checkx}_n . \]

\[
\begin{align*}
\psi_T(\nu x . P) & = \quad \psi_T(P) \\
\psi_T(0) & = \quad \text{return} () \\
\psi_T(\text{Stop}) & = \quad \text{takeMVar stop} \\
\psi_T(P | Q) & = \psi_T(P) | \psi_T(Q) \\
\psi_T(!P) & = \nu f. (f | f = \text{do} \{ \text{forkIO} \phi_T(P); f \})
\end{align*}
\]

**Fig. 13.** Induced translations \( \psi_T \) and \( \psi_{0,T} \) for a gstb-translation \( T \) using \( n \) check-MVars.

### B.2 Correctness of Translations with Global Names

In the correctness proof of translation \( \tau \) and \( \tau_0 \), we defined the top-down translation \( \sigma, \sigma_0 \). For showing correctness of induced translations \( \phi_T \), we follow a similar way, and define induced top-down translations \( \psi_T \) and \( \psi_{0,T} \) in Fig. 13, for a given gstb-translation \( T = (T_{\text{send}}, T_{\text{receive}}) \) using \( n \) check-MVars.

We first show the strong relation between \( \phi_T \) and \( \psi_T \):

**Proposition B.1.** For all \( P \in \Pi_{\text{Stop}} \) i) \( \phi_{0,T}(P) \xrightarrow{sr,*} \psi_{0,T}(P) \), ii) \( \Leftarrow \phi_T(P) \xrightarrow{sr,*} \psi_T(P) \), iii) \( \psi_T(P) \sim_c \Leftarrow \phi_T(P) \) and iv) \( \psi_{0,T}(P) \sim_c \phi_{0,T}(P) \).

**Proof.** Parts i) and ii) can be proved similar to Lemma A.3. Part i) is completely analogous, while for part ii) the translation of the \( \nu \)-operator is different, and thus the construction in the induction for this case is

\[
\begin{align*}
\Leftarrow \phi_T(\nu x . P) & \xrightarrow{sr, \text{nmvar} \rightarrow sr, \text{beta} \rightarrow sr, \text{tmvar} \rightarrow sr, \text{beta} \rightarrow n+1} sr, \text{mkbinds} \\
& \quad \nu x, \text{contx}, \text{checkx}_1, \ldots, \text{checkx}_n . \\
& \quad (\Leftarrow \phi_T(P) | \text{contx m} - | \text{checkx}_1 m - | \ldots | \text{checkx}_n m - | x = \text{Channel contx checkx}_1 \ldots \text{checkx}_n)
\end{align*}
\]

where \( n \) is the number of check-MVars and where all \( (sr, \text{tmvar}) \)-steps are deterministic. Parts iii) and iv) follow from Proposition 3.6 analogously to the proof of Proposition A.4.

Now, we can prove preservation of may-convergence for any induced translation of an executable gstb-translation, and preservation of may-divergence, provided that must-divergence equivalence already holds:

**Proposition B.2.** Let \( T \) be an executable gstb-translation. Let \( P \) be a closed \( \Pi_{\text{Stop}} \)-process.

- If \( P \downarrow \) then \( \phi_{0,T}(P) \downarrow \).
- If for all closed \( Q \in \Pi_{\text{Stop}} \): \( Q \uparrow \iff \psi_{0,T}(Q) \uparrow \) then the implication \( P \uparrow \implies \phi_{0,T}(P) \uparrow \) holds.
Proof. The proof of the first item is analogous to the proof of Proposition A.9 by an induction on the given converging reduction sequence for $P$, that constructs a converging sequence for $\psi_{0,T}(P)$ and then applies Proposition B.1 to derive the sequence for $\phi_{0,T}(P)$. The second item follows also by an induction on the given reduction sequence for $P$ to a must-divergent process, where the base case is covered by the precondition.

We sketch the main steps, required to perform the induction:

– For the base case, one can verify that if a closed process $P$ of the calculus $\Pi_{\text{Stop}}$ is successful, then $\psi_{0,T}(P) \rightarrow^\text{sr,\ast} Q$ where $Q$ is successful.

– For the translation of $\text{dia}\rightarrow$-steps the following claim hold:

Let $P \in \Pi_{\text{Stop}}$ be a closed process with $P \rightarrow^\text{dia} P'$. Then there is a sequence $\psi_{0,T}(P) \rightarrow^\text{sr,\ast} \psi_{0,T}(P')$ in the CH-calculus.

The proof is similar to the proof of Lemma A.5 where one can verify that

- the sr-reduction for the CH-program belonging to $\text{putS}$ or $\text{putC}$ consists of $\text{sr,case} \rightarrow^\text{beta}$
- the sr-reduction for the CH-program belonging to $\text{takeS}$ or $\text{takeC}$ consists of $\text{sr,case} \rightarrow^\text{beta}$

and that no garbage (in difference to Lemma A.5) is generated, and that after executing sender and receiver program the requirements on an executable gstb-translation imply that the corresponding MVars are again empty.

– For other reduction steps in the $\Pi_{\text{Stop}}$-calculus the following claim can be proved analogous to Lemma A.6 with a different translation of $\nu x$, but this does not change the result:

Let $P, P' \in \Pi_{\text{Stop}}$ be closed, such that $P \rightarrow^\text{dsc} P'$. Then $\psi_{0,T}(P) \rightarrow^\text{sr,\ast} \psi_{0,T}(P')$, if rule (replunfold) is used, and $\psi_{0,T}(P) \equiv \psi_{T}(P')$, otherwise. In particular, $\psi_{0,T}(P) \sim_c \psi_{0,T}(P')$.

To prove convergence equivalence for $\phi_{0,T}$ for a given gstb-translation it thus suffices to show, that $\psi_{0,T}$ reflects may-convergence and may-divergence.

B.3 Correctness of a Translation with the Interprocess Check Restriction and Using Three Global Check-MVars

We sketch that the first translation in Table 2 is correct. The translation is

$$\xi_1 = (T_{\text{send}}, T_{\text{receive}}) = ([\text{putS, putC}^1, \text{takeC}^2, \text{putC}^3], [\text{takeC}^1, \text{putC}^2, \text{takeC}^3, \text{takeS}])$$

We conjecture that all four translations are correct using similar arguments. We leave open whether the translations 5,6 of Table 2 are correct. In the proof we mention only the reduction steps that correspond to actions.

Theorem B.3. For all closed $\pi$-processes $P$ and the translation $\phi_{0,\xi_1}$, the equivalences $P\downarrow \iff \phi_{0,\xi_1}(P)\downarrow$ and $P\downarrow \iff \phi_{0,\xi_1}(P)\downarrow$ hold.

Proof. 1. If $P\downarrow$, then $\phi_{0,\xi_1}(P)\downarrow$ follows from Proposition B.2.
2. Let us assume that $\psi_{0,1}(P) \downarrow$, and let $\psi_{0,1}(P) \xrightarrow{*} s_0$ be a successful reduction sequence. The task is now to find a rearrangement of the reduction sequence which remains successful, and that can be translated back into a $\Pi_{\text{Step}}$-calculus reduction sequence. We only consider the MVar accesses ignoring the other reduction steps and try to make an appropriate rearrangement. We can assume that there is an access to an MVar, say $x$, in the reduction sequence. Due to the initialization, the first one is a putMVar on $x$.

If the sequence $\text{put}C_x, \text{take}C_x, \text{put}C_x^3$ in the thread of putS$_x$ is not finished, then this thread does not contribute to the success. Also the $T_{\text{receive}}$-sequence cannot be finished. Hence we can remove all the reductions from both MVar-access-sequences without changing the success. This makes the reduction sequence smaller and we can use induction. If the $T_{\text{send}}$-sequence is complete, but the takeS$_x$ of the $T_{\text{receive}}$-sequence is missing, then it is impossible that another $T_{\text{receive}}$-sequence starts (see Proposition 5.8) Hence we can add the takeC$_x, \text{take}S_x$-sequence or only the action takeS$_x$ without changing success and thus the two sequences are completely done, and retranslatable. We can move the reductions as far to the left as possible, close to the start of $T_{\text{send}}$ with putS$_x$. Then we can apply induction on the remaining part of the reduction sequence.

Thus we have $\psi_{0,1}(P) \downarrow \implies P \downarrow$. By Proposition B.1 this also shows $\phi_{0,1}(P) \downarrow \implies P \downarrow$.

3. Now we can assume that $P \downarrow \iff \psi_{0,1}(P) \downarrow$, and also $P \uparrow \iff \psi_{0,1}(P) \uparrow$.

4. The implication $P \uparrow \implies \phi_{0,1}(P) \uparrow$ now follows from Proposition B.2.

5. The implication $\phi_{0,1}(P) \uparrow \implies P \uparrow$ is a bit more complex and requires again rearranging the reduction sequence. We show $\psi_{0,1}(P) \uparrow \implies P \uparrow$ and apply Proposition B.2 to derive the result for $\phi_{0,1}(P) \uparrow$.

However, we now have to argue more in the case of reduction subsequences that do not correspond to a translated interact. Therefore we have to show that the removal of certain reduction steps and/or the addition of reduction steps is invariant w.r.t. must-divergent processes.

Due to Proposition 5.8, it is sufficient to consider a reduction sequence that exhibits may-divergence, and look for the leftmost single MVar accesses:

Let $\psi_{0,1}(P) \xrightarrow{x:*} s_{\infty}$ be a reduction sequence where $s_{\infty}$ is must-divergent.

- Let there be a leftmost putS$_x$ for channel $x$, and also perhaps a subsequence putC$_x^3$, but no more actions for $x$ in the sequence. Let $s_2$ be the process after performing putS$_x$ (or putC$_x^3$, if present), and $s_1$ be the process before performing putS$_x$. Then we distinguish two cases: i) If there is no other thread which can perform takeC$_1$, then we can remove the operations, constructing a $s'_{\infty}$ which still must be must-divergent: Any other send-sequence on $x$ starting with putS$_x$ performed by another thread also cannot be completed, since there is no receiver that can perform takeC$_1$; ii) There is another thread which can perform takeC$_1$ (but does not do it). Then this thread is blocked and still present in $s_{\infty}$. Thus we can add the step after $s_{\infty}$ and derive an $s'_{\infty}$ and then we shift the reductions that perform the takeC$_1$ to the left.
– The next case is the execution of `putS_x` and `putC^1_x` in thread 1, and `takeC^1_x` in thread 2, but no further actions in the reduction sequence. Now it is not possible to argue using removal of the steps, since there may be a may-convergent reduction sequence using another thread for `takeC^1_x`. Now Proposition 5.8 can be used. If we look for a possible reduction sequence from \( s_\infty \), then there is one reduction sequence (almost sr-reduction sequence if we ignore the main thread) that executes the complete interact for \( x \). Since \( s_\infty \) is must-divergent, we can add reduction steps and the result will be must-divergent. Hence we can prolong the reduction sequence and also by shifting the missing reduction to the left obtain a sequence where the translated interact is complete.

– the final case is that we have the 8 actions for channel \( x \) in two threads. These can be shifted to the left, since there is no disturbance with other threads, and we obtain a complete interact that is retranslatable.

B.4 Correctness of a Translation without the Interprocess Check Restriction and Two Global Check-MVars

We sketch that the translation \( \mathcal{T}_7 \) in Table 3 is correct. The translation is

\[
\mathcal{T}_7 = \langle T_{\text{send}}, T_{\text{receive}} \rangle = ([\text{putC}_1, \text{putS}, \text{takeC}^2, \text{takeC}^1], [\text{takeS}, \text{putC}^2])
\]

We conjecture that the second translation in Table 3 can be proved correct using similar arguments. We mention only the reductions that correspond to actions.

**Theorem B.4.** For all \( \pi \)-processes \( P \) and the translation \( \phi_0, \mathcal{T}_7 \), the equivalences \( P \downarrow \iff \phi_0, \mathcal{T}_7(P) \downarrow \) and \( P \uparrow \iff \phi_0, \mathcal{T}_7(P) \uparrow \) hold.

**Proof.** We can reason similar to the proof of Theorem B.3. Concentrating on the core of the proof, it suffices to show that \( \psi_{\mathcal{T}_7}(P) \downarrow \implies P \downarrow \) and \( \psi_{\mathcal{T}_7}(P) \uparrow \implies P \uparrow \), where for the second implication we can assume that \( Q \uparrow \iff \psi_{\mathcal{T}_7}(Q) \uparrow \) holds for all closed \( \Pi \text{-Stop} \)-processes \( Q \).

1. Let us assume that \( \psi_{\mathcal{T}_7}(P) \), and let \( \psi_{\mathcal{T}_7}(P) \xrightarrow{\sigma} s_0 \) be a successful reduction sequence. The task is now to find a rearrangement of the reduction sequence which remains successful, and that can be translated back into a \( \Pi \text{Stop} \)-calculus reduction sequence. We only consider the MVar accesses ignoring the other reduction steps and try to make an appropriate rearrangement. We can assume that there is an access to an MVar, say \( x \), in the reduction sequence. Due to the initialization, the first one is a `putC^1_x`. If the sequence `putS_x, takeC^2_x, takeC^1_x` in the thread of `putC^1_x` is not finished, then this thread does not contribute to the success. Also the `T_{\text{receive}}` sequence cannot be finished. Hence we can remove all the reductions from both MVar-access-sequences without changing the success. This makes the reduction sequence smaller and we can use induction. If the `T_{\text{send}}`-sequence is complete, but the `takeS_x` of the `T_{\text{receive}}`-sequence is missing, then it is impossible that another `T_{\text{receive}}`-sequence starts (see Proposition 5.10). Hence
we can add the $\text{take}_x$, $\text{put}^{C_2}_x$-sequence without changing success and thus the two sequences are completely done, and retranslatable. We can move the reductions as far to the left as possible, close to the start of $T_{send}$ with $\text{put}^{C_1}_x$. Then we apply induction on the remaining part of the reduction sequence.

2. The implication $\psi_{\tau_x}(P)^{\uparrow} \implies P^{\uparrow}$ requires again rearranging the reduction sequence. However, we now have to argue more in the case of reduction subsequences that do not correspond to a translated interact. Therefore we have to show that the removal of certain reduction steps and/or the addition of reduction steps is invariant w.r.t. must-divergent processes. Due to Proposition 5.10, it is sufficient to consider a reduction sequence that exhibits may-divergence, and look for the leftmost single MVar accesses: Let $\psi_{\tau_x}(P)^{\rightarrow^*} s_\infty$ be a reduction sequence where $s_\infty$ is must-divergent.

- Let there be a leftmost $\text{put}^{C_1}_x$ for channel $x$, and also perhaps a subsequence $\text{put}^{S}_x$, but no more actions for $x$ in the sequence. If there is no thread that could perform $\text{take}_x$, then we can remove the $\text{put}^{C_1}_x$ and $\text{put}^{S}_x$-executions and the adapted process $s_\infty$ remains to be must-divergent, since any other sender that performs $\text{put}^{C_1}_x$ also cannot be completed. If there is a thread that can perform $\text{take}_x$, then this is still possible in $s_\infty$. Thus we add the $\text{take}_x$ (and the $\text{put}^{S}_x$ if needed) for $s_\infty$ and then shift the corresponding reductions to the left. Then we can use another case of this proof.

- The next case is the execution of $\text{put}^{C_1}_x$ and $\text{put}^{S}_x$ in thread 1, and $\text{take}_x$ in thread 2, but no further actions in the reduction sequence. Now Proposition 5.10 implies that if we look for a possible reduction sequence from $s_\infty$, then there is one reduction sequence (almost sr-reduction sequence if we ignore the main thread) that executes the complete interact for $x$. Since $s_\infty$ is must-divergent, we can add reduction steps and the result will be must-divergent. Hence we can prolong the reduction sequence and also by shifting the missing reduction to the left obtain a sequence where the translated interact is complete.

- The next case is the execution of $\text{put}^{C_1}_x$ and $\text{put}^{S}_x$ (and perhaps $\text{take}^{C_2}_x$) in thread 1, and $\text{take}_x$, $\text{put}^{C_2}_x$ in thread 2, but no further actions in the reduction sequence. Again Proposition 5.10 implies that if we look for a possible reduction sequence from $s_\infty$, then there is one reduction sequence (almost sr-reduction sequence if we ignore the main thread) that executes the complete interact for $x$. Since $s_\infty$ is must-divergent, we can add reduction steps and the result will be must-divergent. Hence we can prolong the reduction sequence and also by shifting the missing reduction to the left obtain a sequence where the translated interact is complete.

- The final case is that we have the 6 actions for channel $x$ in two threads. These can be shifted to the left, since there is no disturbance with other threads, and we obtain a complete interact that is retranslatable.
Proposition 3.5. Let \( P_1, P_2 \) be well-formed and \( P_1 \equiv P_2 \). Then \( P_1 \sim_c P_2 \).

**Proof.** Let \( P_1, P_2 \) be well-formed, \( P_1 \equiv P_2 \), and \( \mathbb{D} \in PCtx_{CH} \). We have to show i) \( \mathbb{D}[P_1] \downarrow \Rightarrow \mathbb{D}[P_2] \downarrow \); ii) \( \mathbb{D}[P_2] \downarrow \Rightarrow \mathbb{D}[P_1] \downarrow \); iii) \( \mathbb{D}[P_1] \downarrow \Rightarrow \mathbb{D}[P_2] \downarrow \); and iv) \( \mathbb{D}[P_2] \downarrow \Rightarrow \mathbb{D}[P_1] \downarrow \). The process \( \mathbb{D}[P_1] \) is successful iff \( \mathbb{D}[P_2] \) is successful, since \( \equiv \) cannot remove or introduce a main-thread. For any process \( P_3 \), we have:
$\mathbb{D}[P_1] \xrightarrow{sr} P_3$ iff $\mathbb{D}[P_2] \xrightarrow{sr} P_3$, since $\equiv$ is a congruence and since $\xrightarrow{sr}$ is closed w.r.t. $\equiv$. Using both facts, we can use induction on a given reduction sequence starting with $\mathbb{D}[P_i]$ and ending in a successful process, to show that $\mathbb{D}[P_i]$ reduces to a successful process, where $(i, j) \in \{(1, 2), (2, 1)\}$. This shows parts i) and ii).

For the remaining parts, we show $\mathbb{D}[P_1] \uparrow \iff \mathbb{D}[P_2] \uparrow$. We use induction on a given reduction sequence starting with $\mathbb{D}[P_i]$ and ending in must-divergent process to show that $\mathbb{D}[P_j] \uparrow$ where $(i, j) \in \{(1, 2), (2, 1)\}$. The base case holds, since parts i) and ii) imply $\mathbb{D}[P_1] \uparrow \iff \mathbb{D}[P_2] \uparrow$ and the induction step again uses the fact, that each reduction step applied to $\mathbb{D}[P_i]$ can also be applied to $\mathbb{D}[P_j]$ for $(i, j) \in \{(1, 2), (2, 1)\}$.

C.3 Embedding CH in CHF

We briefly recall the program calculus CHF [25,26] and then provide an embedding of CH into CHF. After showing that the embedding is adequate w.r.t. contextual semantics we transfer results on the correctness of program transformations from CHF to CH. Since the calculus CHF is quite similar to the calculus CH, we often only comment on the differences between both calculi.

The syntax of processes, expressions, monadic expressions and types is given in Fig. 15. The laws of structural congruence and the syntax of contexts are given in Figs. 16 and 17. The calculus CHF uses the same types as the calculus CH. Expressions in CHF are like expressions in CH, with the only difference that the monadic operator forkIO is replaced by the monadic operator future. While forkIO is of type $IO () \rightarrow IO ()$ the operator future is of type $IO t \rightarrow IO t$ for any type $t$. On the process-level CH threads $\leftarrow e$ are replaced by futures $x \leftarrow e$ where $x$ is an introduced variable, called the name of the future. The operational behavior is, that expression $e$ (of type $IO t$) is evaluated concurrently and thereafter the result (of type $t$) is bound to variable $x$, which makes the value of the concurrent computation accessible. In the standard reduction of the calculus CHF (see Fig. 18) this is implemented by the following changes w.r.t. the standard reduction in CH: the rule (fork) creates the new future (instead of a new thread) and the calling thread receives the name of the future as result. If a concurrent thread finishes its computation, then the result is shared as a global binding and the thread is removed by the (new) rule (unIO).

The main thread in CHF is a future and it is written as $x \leftarrow e$. Definitions of functional values, monadic values, and values are as in CH adapted to the syntax changes. A well-formed process $P$ is successful, if $P \equiv \nu x_1 \ldots \nu x_n.(x \leftarrow e \text{return } e \text{ if } P')$. The definitions of may-convergence and should-convergence in CHF are the same as in CH (see Definition 3.2), but adapted to the syntax changes and using the standard reduction defined in Fig. 18. Contextual approximation $\preceq_c$ and contextual equivalence $\sim_c$ on CHF-processes and expressions are defined analogous to Definition 3.3. Well-formed and structural congruent CHF-processes $P_1 \equiv P_2$ are also contextual equivalent (i.e. $P_1 \sim_c P_2$, see [25]).
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$P \in \text{Proc}_{\text{CHF}} ::= (P_1 | P_2) | x \leftarrow e | \nu x.P | x \in e | x \in m - | x = e$

$e \in \text{Expr}_{\text{CHF}} ::= x | m | \lambda x.e | (e_1 e_2) | \text{seq} e_1 e_2 | c e_1 \ldots e_{n_{\text{ar}(c)}}$

\begin{align*}
\text{letrec} & \ x_1 = e_1, \ldots, x_n = e_n \ \text{in} \ e \\
\text{case} & \ e \ of \ (c\nu x_1 \ldots x_{n_{\text{ar}(c)}} \rightarrow e_1) \ldots (c\nu y \rightarrow e_{n_2}) \\
\end{align*}

$m \in \text{MExpr}_{\text{CHF}} ::= \text{return} e | e \gg e' | \text{future} e | \text{takeMVar} e | \text{newMVar} e | \text{putMVar} e e'$

$t \in \text{Typ}_{\text{CHF}} ::= \text{Id} t | (T \ t_1 \ldots t_n) | \text{MVar} t | t_1 \rightarrow t_2$

Fig. 15. Syntax of expressions, processes, and types of CHF

$P_1 | P_2 \equiv P_1 = P_2$

$(P_1 | P_2) | P_3 \equiv P_1 | (P_2 | P_3)$

$(\nu x.P_1) | P_2 \equiv \nu x.(P_1 | P_2) \text{ if } x \notin \text{FV}(P_2)$

$\nu x_1.\nu x_2.P \equiv \nu x_2.\nu x_1.P$

$P_1 \equiv P_2 \text{ if } P_1 \alpha \equiv P_2$

Fig. 16. Structural congruence of CHF

$\mathbb{D} \in \text{PCtxt}_{\text{CHF}} ::= [] | \mathbb{D} | P | P \mathbb{D} | \nu x.\mathbb{D}$

$M \in \text{MCtxt}_{\text{CHF}} ::= [] | M \gg e$

$\exists \in \text{FCtxt}_{\text{CHF}} ::= E \ | \ \text{(takeMVar} E \ | \ \text{(putMVar} E e \ e)\}$

$E \in \text{ECtxt}_{\text{CHF}} ::= [] | (E \ e) \ | \ (\text{seq} \ E \ e) \ | \ (\text{case} \ E \ e \ of \ \text{alts})$

Fig. 17. $\text{PCtxt}_{\text{CHF}}$, $\text{MCtxt}_{\text{CHF}}$, $\text{FCtxt}_{\text{CHF}}$, $\text{ECtxt}_{\text{CHF}}$-contexts

Monadic Computations

| lunit | $\nu \ e \leftarrow M[\text{return} c_1 \gg e_2] \xrightarrow{sr} y \leftarrow M[e_2 e_1]$ |
| tmvar | $y \leftarrow M[\text{takeMVar} x] | x \in e \xrightarrow{sr} y \leftarrow M[\text{return} e] | x \in m -$ |
| pmvar | $y \leftarrow M[\text{putMVar} x e] | x \in m - \xrightarrow{sr} y \leftarrow M[\text{return} e] \ | x \in e$ |
| nmvar | $y \leftarrow M[\text{newMVar} e] \xrightarrow{sr} \nu z.(y \leftarrow M[\text{return} z] | x \in e$ |
| fork | $y \leftarrow M[\text{future} e] \xrightarrow{sr} \nu z.(y \leftarrow M[\text{return} z] | x \in e$ where z is fresh |
| unIO | $y \leftarrow \text{return} e \xrightarrow{sr} y = e \text{ if the thread is not the main-thread}$ |

Functional Evaluation

| cpo | $y \leftarrow M[\text{letrec} x_1 = e_1, \ldots, x_n = e_n \ | e] \xrightarrow{sr} y \leftarrow M[e_1 e_2] | x = e$ |
| mkbinds | $y \leftarrow M[\text{letrec} x_1 = e_1, \ldots, x_n = e_n \ | e] \xrightarrow{sr} y \leftarrow M[e_1 e_2] | x_1 = e_1 \ldots x_n = e_n)$ |
| beta | $y \leftarrow M[\text{case} \ (\lambda x.e_1) \ | e_2] \xrightarrow{sr} y \leftarrow M[e_1 e_2 / e]$ |
| case | $y \leftarrow M[\text{case} \ (\lambda x.e_1) \ | e_2] \ | \ldots (c \ y_1 \ldots y_n \rightarrow e) \ldots] \xrightarrow{sr} y \leftarrow M[e_1 / y_1, \ldots, e_n / y_n)]$ |
| seq | $y \leftarrow M[\text{seq} v e] \xrightarrow{sr} y \leftarrow M[e] \text{ where } v \text{ is a functional value}$ |

Closure: If $P_1 \equiv \mathbb{D}[P'_1], P_2 \equiv \mathbb{D}[P'_2]$, and $P'_1 \xrightarrow{sr} P'_2$ then $P_1 \xrightarrow{sr} P_2$.

Capture avoidance: We assume capture avoiding reduction for all reduction rules.

Fig. 18. Standard reduction rules of CHF (call-by-name-version)
\[(gc)\] \nu x_1, \ldots, x_n. (P \mid \text{Comp}(x_1) \ldots \mid \text{Comp}(x_n)) \to P,
if \forall i \in \{1, \ldots, n\} : \text{Comp}(x_i) is a binding \(x_i = e_i\), an MVar \(x_i \in e_i\) or \(x_i \in -\), and \(x_1, \ldots, x_n \notin FV(P)\)

\[(dtmvar)\] \nu x. \{\text{takeMVar } x \mid x \in e\} \to \nu x. \{\text{return } e \mid x \in e\},
if \forall \mathbb{D}' \in \text{PTxt}_{CHF} the first execution of \((\text{takeMVar } x)\) is in thread \(y\) for all sr-reductions starting with \(\mathbb{D'}[\nu x. \{\text{takeMVar } x \mid x \in e\}]\)

\[(dpmvar)\] \nu x. \{\text{putMVar } x \mid x \in e\} \to \nu x. \{\text{return } () \mid x \in e\},
if \forall \mathbb{D}' \in \text{PTxt}_{CHF} and any \(e'\) the first execution of \((\text{putMVar } x \mid x \in e')\) for all sr-reductions starting with \(\mathbb{D}'[\nu x. \{\text{putMVar } x \mid x \in e\}]\) is in thread \(y\).

Fig. 19. Transformations \((gc)\), \((dtmvar)\), and \((dpmvar)\)

Figure 19

Program transformations \(\eta\) in \(CHF\) are binary relations between \(CHF\)-processes, where \(\eta\) is correct iff \(\eta \subseteq \sim\). We define garbage collection \((gc)\) and deterministic variants of \((tmvar)\) and \((pmvar)\) for the calculus \(CHF\) in Fig. 19

**Proposition C.1 [25].** The transformations \((\text{unit})\), \((\text{nmvar})\), \((\text{fork})\), \((\text{unIO})\), \((\text{cpce})\), \((\text{mkbinds})\), \((\beta)\), \((\text{case})\), \((\text{seq})\) \((gc)\), \((dtmvar)\), and \((dpmvar)\) are correct program transformations, whereas \((pmvar)\) and \((tmvar)\) in general are not correct as program transformations.

We define an embedding \(\iota\) which embeds \(CH\) into \(CHF\):

**Definition C.2.** We define the translation \(\iota : \text{CH} \to \text{CHF}\). For most of the cases \(\iota\) is applied homomorphically over the term structure, without changing the syntax, i.e. for variables \(r\) and nullary constructors \(r\): \(\iota(r) := r\) and for any syntactic operator \(f\) and arguments \(r_1, \ldots, r_n\): \(\iota(f \ r_1 \ldots \ r_n) := f \ i(r_1) \ldots i(r_n)\).

We now explicitly list the exceptional cases: For expressions \(\text{forkIO } e\) we define:

\[\iota(\text{forkIO } e) := (\text{future } \iota(e)) \gg \text{return } ()\]

The exceptional cases on the process level are

\[\iota(\text{future } e) := \nu x. x \ll \iota(e)\text{ and }\iota(\text{return } e) := \nu x. x \ll \iota(e)\]

where for both cases, we assume that \(x\) is chosen (fresh) such that \(x\) does not occur in \(\iota(e)\). On types, the translation \(\iota\) is the identity.

**Proposition C.3.** Assume that the countable infinite set of variables in \(CH\) is \(\text{Var}_{CH}\) and in \(CHF\) is \(\text{Var}_{CHF} = \text{Var}_{CH} \cup \text{Var}'\) where \(\text{Var}_{CH} \cap \text{Var}' = \emptyset\), \(\text{Var}'\) is countably infinite and translation \(\iota\) always uses a variable of \(\text{Var}'\) for translating \(\ll e\) or \(\ll e\).

Then the translation \(\iota\) is compositional, i.e. \(\iota(C[e]) = \iota(C)[\iota(e)]\).

For analyzing the correspondence between the sr-reductions in \(CH\) and \(CHF\) w.r.t. \(\iota\), an observation is that for all \(\mathbb{D} \in \text{PTxt}_{CH}\): \(\iota(\mathbb{D}) \in \text{PTxt}_{CHF}\); for all \(\mathbb{M} \in \text{MCtxt}_{CH}\): \(\iota(\mathbb{M}) \in \text{MCtxt}_{CHF}\); for all \(\mathbb{E} \in \text{ECTxt}_{CH}\): \(\iota(\mathbb{E}) \in \text{ECTxt}_{CHF}\); and for all \(\mathbb{F} \in \text{FCtx}_{CH}\): \(\iota(\mathbb{F}) \in \text{FCtx}_{CHF}\). We analyze the sr-reduction w.r.t. \(\iota\).
Lemma C.4. Let \( P, P' \) be \( \text{CH} \)-processes with \( P \xrightarrow{\sigma} P' \). Then either \( \iota(P) \xrightarrow{\sigma} \iota(P') \) or \( \iota(P) \xrightarrow{\sigma,\alpha} \iota(P') \) where in the latter case \( \iota(P) \sim_c \iota(P') \).

Proof. We consider the different reduction rules. For rules (lunit), (tmvar), (pmvar), (mvar), and for all functional evaluations, we have: if \( P \xrightarrow{\sigma} P \) then \( \iota(P) \xrightarrow{\sigma} \iota(P') \). For (fork), let \( P = \mathcal{D}[\varepsilon = \text{M}[\text{forkIO } e]] \xrightarrow{\sigma} \mathcal{D}[\varepsilon = \text{M}[\text{return } ()] \mid \varepsilon = e] = P' \). Then we can reduce \( \iota(P) \) as follows:

\[
\iota(P) = \iota(\mathcal{D})[\nu x. x \leftarrow \text{future } (\iota(e)) \gg \lambda \rightarrow \text{return } ()] \\
\xrightarrow{\sigma, \text{fork}} \iota(\mathcal{D})[\nu x. \nu z. x \leftarrow \text{return } z \gg \lambda \rightarrow \text{return } () | z \leftarrow \iota(e)] \\
\xrightarrow{\sigma, \text{lunit}} \iota(\mathcal{D})[\nu x. \nu z. x \leftarrow \text{return } () | z \leftarrow \iota(e)] = \iota(P')
\]

Theorem C.1 implies \( \iota(P) = \iota(P') \).

Lemma C.5. \( P \in \text{Proc}_{\text{CH}} \) is successful iff \( \iota(P) \) is successful.

Proof. If \( P \in \text{Proc}_{\text{CH}} \) is successful, then \( P \equiv \mathcal{D}[\varepsilon = \text{return } ()] \) and \( \iota(P) = \iota(\mathcal{D})[\nu x. x \leftarrow \text{return } ()] \). If \( \iota(P) \) is successful, then \( \iota(P) = \mathcal{D}[\varepsilon \leftarrow \text{return } ()] \) and thus \( P \equiv \mathcal{D}'[\varepsilon \leftarrow \text{return } ()] \) for some \( \mathcal{D}' \in \text{Pctx}_{\text{CH}} \).

Proposition C.6. Let \( P \in \text{Proc}_{\text{CH}} \) such that \( P \downarrow \). Then \( \iota(P) \downarrow \).

Proof. By induction on \( P \xrightarrow{\sigma} P' \) where \( P' \) is successful. The base case holds by Lemma C.5. For the induction step, let \( P \xrightarrow{\sigma} P'' \xrightarrow{\sigma, \alpha} P' \). The induction hypothesis shows \( \iota(P'') \downarrow \) and Lemma C.4 shows \( \iota(P) \xrightarrow{\sigma, \alpha} \iota(P'') \). Thus \( \iota(P) \downarrow \).

Checking all cases of the standard reduction in \( \text{CHF} \) together with inspecting the images of the translation \( \iota \) shows:

Lemma C.7. Let \( P \in \text{Proc}_{\text{CH}} \), \( \iota(P) \xrightarrow{\sigma, \alpha} P' \), and \( \alpha \not\in \{\text{fork, unIO}\} \). Then there exists \( P'' \) with \( P \xrightarrow{\sigma} P'' \) such that \( \iota(P'') = P' \), i.e. the diagram shown on the right side holds.

Lemma C.8. Let \( P \in \text{Proc}_{\text{CH}} \) such that \( \iota(P) \xrightarrow{\sigma, \text{fork}} P' \). Then there exists a \( \text{CH} \)-process \( P'' \) where \( P \xrightarrow{\sigma, \text{fork}} P'' \xrightarrow{\sigma, \alpha} P' \), and \( P' \xrightarrow{\sigma, \alpha} \iota(P'') \). The equivalence \( \iota(P'') \sim_c P' \) holds. The diagram on the right hand side shows the given and existentially quantified reductions and translations steps.

Proof. The \( \sigma, \text{fork} \)-step in \( \text{CHF} \) evaluates a \text{future}-operation that must stem from translating a \text{forkIO} \( e = \text{future } (\iota(e)) \gg \text{return } () \), the reduction \( \iota(P) \xrightarrow{\sigma, \text{fork}} P' \) can be extended by two reduction steps \( P' \xrightarrow{\sigma, \text{lunit}} P'' \xrightarrow{\sigma, \text{alpha}} P' \) such that \( P \xrightarrow{\sigma, \text{fork}} P'' \). Since \( \iota(P'') = P' \), we have \( P'' \sim_c P' \). □
The two reductions leading from \( P' \) to \( \iota(P'') \) in the previous lemma, are correct program transformations, and they are deterministic for the corresponding thread (i.e. for this thread, no other reduction is possible, even if other components or threads change (for instance, the content of MVars). Thus, we can safely add all the missing reduction steps corresponding to Lemma C.8 to a given sequence \( \iota(P) \xrightarrow{\text{sr,unIO}} P_0 \) resulting in a sequence \( \iota(P) \xrightarrow{\text{sr,unIO}} P_1 \) such that i) \( P_0 \sim_c P_1 \) and ii) \( P_1 \) is successful if \( P_0 \) is successful.

Lemmas C.7 and C.8 do not cover the case where \( \iota(P) \xrightarrow{\text{sr,unIO}} P' \) for some \( P \in \text{Proc}_{CH} \). In this case \( P = \text{D}[\text{return } ()] \) must hold, and \( \iota(P) = \iota(\text{D})[\nu.x \leftarrow \text{return } ()] \xrightarrow{\text{sr,unIO}} \iota(\text{D})[\nu.x \leftarrow ()] \). Since future \( x \) cannot be used in the context \( \iota(\text{D}) \), it is irrelevant, if the (sr,unIO)-reduction is performed or is not performed. I.e., given a sequence \( \iota(P) \xrightarrow{\text{sr,unIO}} P_1 \), we can modify the sequence, by removing all (sr,unIO)-reduction, resulting in a sequence \( \iota(P) \xrightarrow{\text{sr,unIO}} P_2 \) such that i) \( P_1 \sim_c P_2 \) and ii) \( P_1 \) is successful if and only if \( P_2 \) is successful.

**Definition C.9.** Let \( \text{Red} = \iota(P) \xrightarrow{\text{sr,unIO}} P' \) be a CHF-standard reduction sequence for \( P \in \text{Proc}_{CH} \). We say that \( \text{Red} \) is \( \iota \)-normalized if the two described transformations on reduction sequences (adding reduction steps corresponding to Lemma C.8 and removing (sr,unIO)-reductions) are not applicable to \( \text{Red} \).

As argued above, the following lemma holds:

**Lemma C.10.** For every sequence \( \iota(P) \xrightarrow{\text{sr,unIO}} P' \) with \( P \in \text{Proc}_{CH} \), there exists a \( \iota \)-normalized reduction sequence \( \iota(P) \xrightarrow{\text{sr,unIO}} P'' \) such that i) \( P' \sim_c P'' \) and ii) \( P'' \) is successful if \( P' \) is successful.

**Proposition C.11.** Let \( P \in \text{Proc}_{CH} \) and \( \iota(P)\downarrow \). Then \( P\downarrow \).

**Proof.** Let \( \iota(P) \xrightarrow{\text{sr,unIO}} P_0 \) where \( P_0 \) is successful. By Lemma C.10 there exists a \( \iota \)-normalized reduction sequence \( \iota(P) \xrightarrow{\text{sr,unIO}} P_1 \) where \( P_1 \) is successful and \( n \geq 0 \). By induction on \( n \) we show \( P\downarrow \). Lemma C.5 covers the case \( n = 0 \). If \( n > 0 \) then \( \iota(P) \xrightarrow{\text{sr,unIO}} P_1 \). Since the given sequence is \( \iota \)-normalized, we can either apply the commutation diagram from Lemma C.7 or the diagram from Lemma C.8 to a prefix of \( \iota(P) \xrightarrow{\text{sr,unIO}} P_1 \) such that \( P \xrightarrow{\text{sr}} P' \) and \( \iota(P') \xrightarrow{\text{sr,unIO}} P_0 \) where \( n' < n \) and \( \iota(P') \xrightarrow{\text{sr,unIO}} P_1 \) is \( \iota \)-normalized and thus we can apply the induction hypothesis which shows \( P''\downarrow \) and thus \( P\downarrow \). 

By Propositions C.6 and C.11 we also have:

**Corollary C.12.** Let \( P \in \text{Proc}_{CH} \). Then \( \iota(P)\uparrow \iff P\uparrow \)

**Proposition C.13.** Let \( P \in \text{Proc}_{CH} \) such that \( P\uparrow \). Then \( \iota(P)\uparrow \).

**Proof.** By induction on length \( n \) of a reduction \( P \xrightarrow{\text{sr,n}} P' \) where \( P'\uparrow \). Corollary C.12 covers the base case. For the induction step, let \( P \xrightarrow{\text{sr}} P'' \xrightarrow{\text{sr,n}} P' \). The induction hypothesis shows \( \iota(P'')\uparrow \); Lemma C.4 shows that \( \iota(P) \xrightarrow{\text{sr,unIO}} \iota(P'') \), and thus \( \iota(P)\uparrow \).
Proposition C.14. Let \( P \in \text{Proc}_{\text{CH}} \) and \( \iota(P) \uparrow \). Then \( P \uparrow \).

Proof. Let \( \iota(P) \xrightarrow{\text{sr},s} P_0 \) where \( P_0 \uparrow \). By Lemma C.10, there exists a \( \iota \)-normalized reduction sequence \( \iota(P) \xrightarrow{\text{sr},n} P_1 \) where \( P_1 \uparrow \) and \( n \geq 0 \). We use induction on \( n \) to show \( P \uparrow \). If \( n = 0 \), then Corollary C.12 shows that \( P \uparrow \). If \( n > 0 \), then \( \iota(P) \xrightarrow{\text{sr},n} P_1 \). Since the sequence is \( \iota \)-normalized, we can either apply the commutation diagram from Lemma C.7 or the diagram from Lemma C.8 to a prefix of \( \iota(P) \xrightarrow{\text{sr},n} P_1 \) such that \( P \xrightarrow{\text{sr}} P'' \) and \( \iota(P'') \xrightarrow{\text{sr},n'} P_0 \) where \( n' < n \) and also \( \iota(P'') \xrightarrow{\text{sr},n'} P_1 \) is \( \iota \)-normalized and thus we can apply the induction hypothesis which shows \( P'' \uparrow \) and thus also \( P \uparrow \).

Propositions C.6, C.11, C.13 and C.14 show:

Theorem C.15. The translation \( \iota \) is convergence equivalent.

Theorem C.16. The translation \( \iota \) is adequate, i.e. for all processes \( P_1, P_2 \in \text{Proc}_{\text{CH}} \): \( \iota(P_1) \sim_c \iota(P_2) \implies P_1 \sim_c P_2 \) and for all expressions \( e_1, e_2 \in \text{Expr}_{\text{CH}} \): \( \iota(e_1) \sim_c \iota(e_2) \implies e_1 \sim_c e_2 \).

Proof. This holds, since \( \iota \) is compositional and convergence equivalent. We only show the part for processes. Let \( \xi \in \{\downarrow, \uparrow\} \), \( (i, j) \in \{(1, 2), (2, 1)\} \). Let \( \iota(P_i) \sim_c \iota(P_j) \) and \( D \in \text{PCtx}_{\text{CH}} \) such that \( D[P_i] \xi \). Then \( \iota(D[P_i]) = \iota(D)[\iota(P_i)] \xi \) (since \( \iota \) is compositional) and thus \( \iota(D)[\iota(P_j)] \xi \) (since \( \iota \) is convergence equivalent). Since \( \iota(P_i) \sim_c \iota(P_j) \), this implies \( \iota(D)[\iota(P_j)] \xi \) which implies \( \iota(D[P_j]) \xi \) (since \( \iota \) is compositional) and thus \( D[P_j] \xi \) (since \( \iota \) is convergence equivalent).

Proposition 3.6. The transformations \( \text{lunit} \), \( \text{nmvar} \), \( \text{fork} \), \( \text{cpce} \), \( \text{mkbinds} \), \( \text{beta} \), \( \text{case} \), \( \text{seq} \), \( \text{gc} \), \( \text{dtmvar} \), and \( \text{dpmvar} \) are correct in \( \text{CH} \).

Proof. For all mentioned transformations \( \eta \), we have: if \( P \xrightarrow{\eta} P' \) then \( \iota(P) \sim_c \iota(P') \) by Proposition C.1 which implies \( P \sim_c P \) by Theorem C.16.

D Barbed Convergence Testing and Equivalence

In [27], the following claim is proved, where \( \sigma \) are name-to-name substitutions:

Theorem D.1 (27). For all processes \( P, Q \in \Pi_{\text{Stop}} \):
- If for all \( \sigma, R : \sigma(P) \mid R \leq_\downarrow \sigma(Q) \mid R \), then \( P \leq_{c,\downarrow} Q \).
- If for all \( \sigma, R : \sigma(P) \mid R \leq_\uparrow \sigma(Q) \mid R \wedge \sigma(P) \mid R \leq_{c,\downarrow} \sigma(Q) \mid R \), then \( P \leq_c Q \).

Example D.2. We show the equivalence \( x(y).\text{Stop} \xrightarrow{\text{πz,Stop}} \sim_c \text{Stop} \). Clearly for all \( \sigma \) and all processes \( R \) we have \( \sigma(\text{Stop}) \mid R \) is successful and thus \( \sigma(\text{Stop}) \mid R \downarrow \) and \( \sigma(\text{Stop}) \mid R \downarrow \). We have that \( \sigma(x(y).\text{Stop} \xrightarrow{\text{πz,Stop}} \mid R \downarrow \), since the process reduces in one step to \( \text{Stop} \mid R \). We observe that it is impossible to reduce \( \sigma(x(y).\text{Stop} \xrightarrow{\text{πz,Stop}} \mid R \) into a must-divergent process, since the process becomes successful if one of the components \( \sigma(x(y).\text{Stop} \) or \( \sigma(\text{πy,Stop}) \) is part of the redex, and otherwise (the interaction between these two processes is always possible). Thus, \( \sigma(x(y).\text{Stop} \xrightarrow{\text{πz,Stop}} \mid R \) is must-convergent. Hence the preconditions of Theorem D.1 hold and the equivalence holds.
For stop-free processes, contextual equivalence coincides with so-called barbed testing equivalence, where the observation is whether a process may- or should-reduces to a process that has a free input on a fixed channel name (see [27]):

**Definition D.3.** Let $Π$ be the subcalculus of $Π_{Stop}$ that does not have the constant $Stop$ as a syntactic construct. Processes, contexts, reduction, structural congruences are accordingly adapted for $Π$.

Let $P ∈ Π$ and $x ∈ N$. A process $P$ has a barb on input $x$ (written $P ↵ x$) iff $P \equiv νX. (x(y)P'|P'')$ where $x \notin X$. We write $P \downarrow x$ iff there exists $P'$ such that $P \xrightarrow{sr,∗} P'$ and $P'|x$. We write $P \parallel x$ iff for all $P'$ with $P \xrightarrow{sr,∗} P'$ also $P'|x$ holds. We write $P \parallel x$ iff $P \downarrow x$ does not hold, and $P \downarrow x$ iff $P \parallel x$ does not hold.

For a name $x ∈ N$, barbed may- and should-testing preorder $≤_{c,barb}$ and barbed may- and should-testing equivalence $∼_{c,barb}$ are defined as $≤_{c,barb} := ≤_{c,l_x} \cap ≤_{c,l_x}$ and $∼_{c,barb} := ≤_{c,barb} \cap (≤_{c,barb})^{-1}$ where for $ξ ∈ \{l_x, l_{∥x}, l_{\downarrow x}, l_{∥x}\}$ and $P, Q ∈ Π$, $P ≤_c ξ Q$ holds iff for all contexts $C ∈ Π : C[P|ξ] ⟷ C[Q|ξ]$.

**Theorem D.4 ([27])**. For all processes $P, Q ∈ Π$: $P ≤_{c,barb} Q \iff P ≤_c Q$, and hence also $P ∼_{c,barb} Q \iff P ∼_c Q$. 